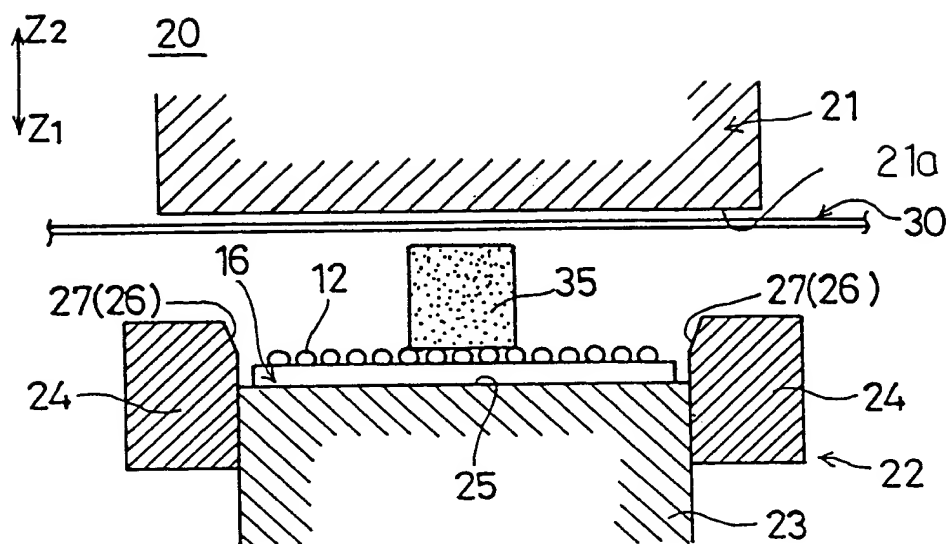


151

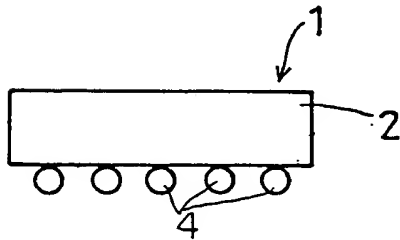
图 1



1 / 1 3 1

7/10/23

Figure 1A shows a single neuron with a soma, dendrites, and an axon, all contained within a rectangular box.



☒ 1 B

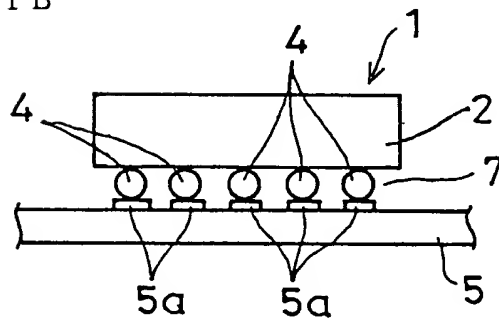
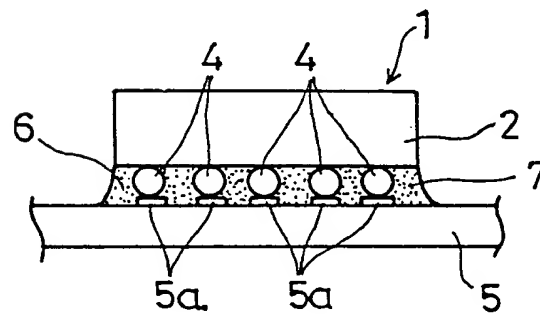
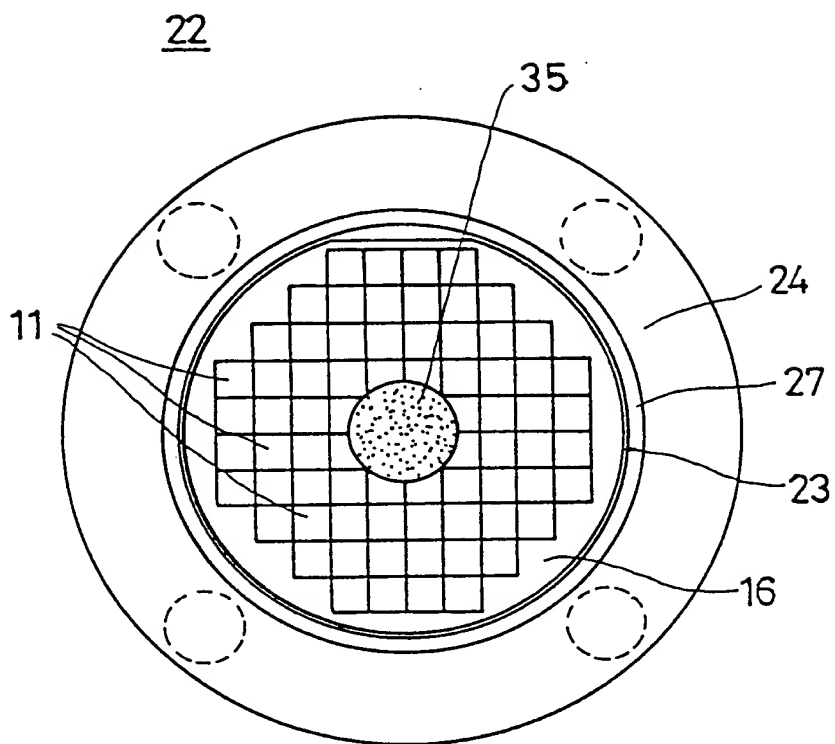
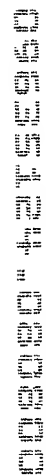
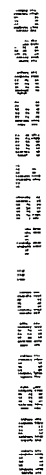


図 1 C

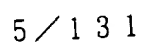


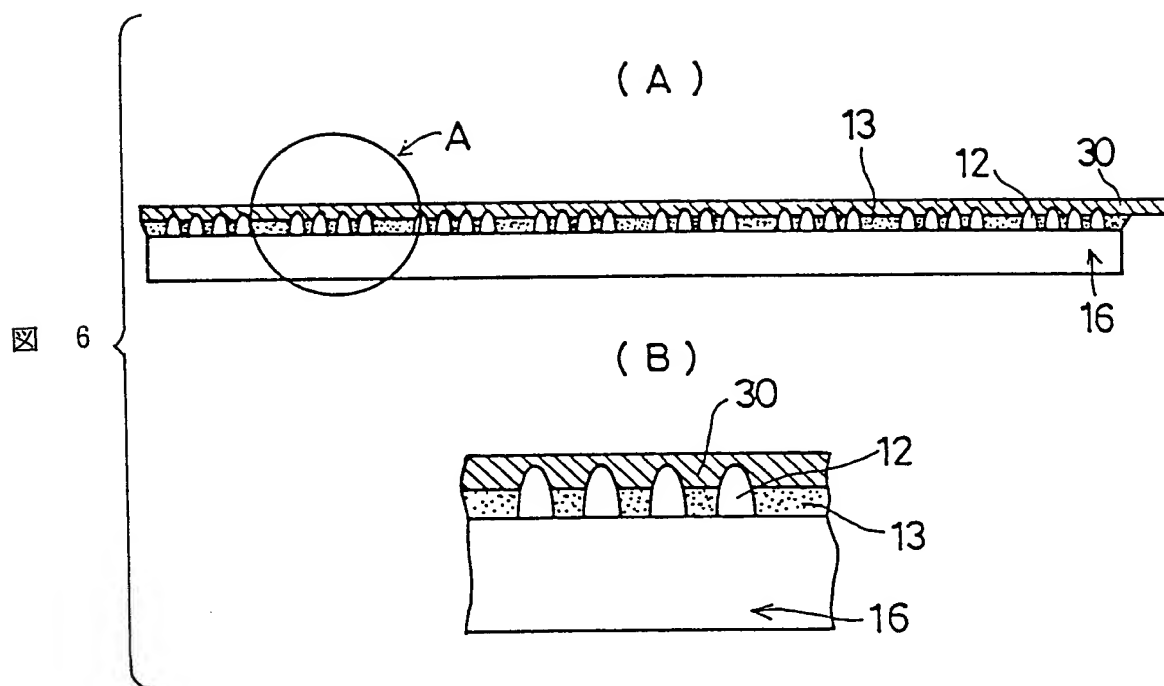
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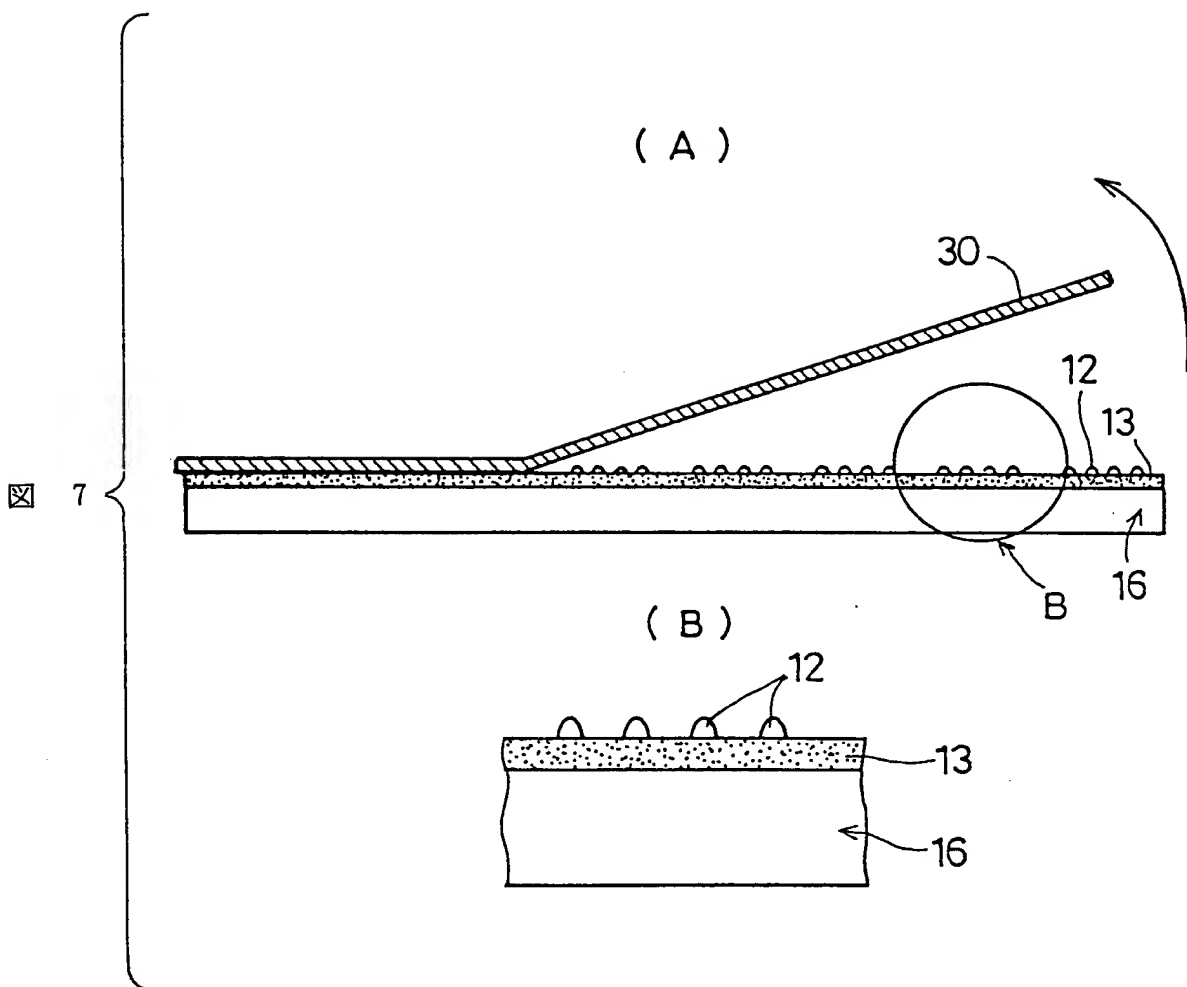


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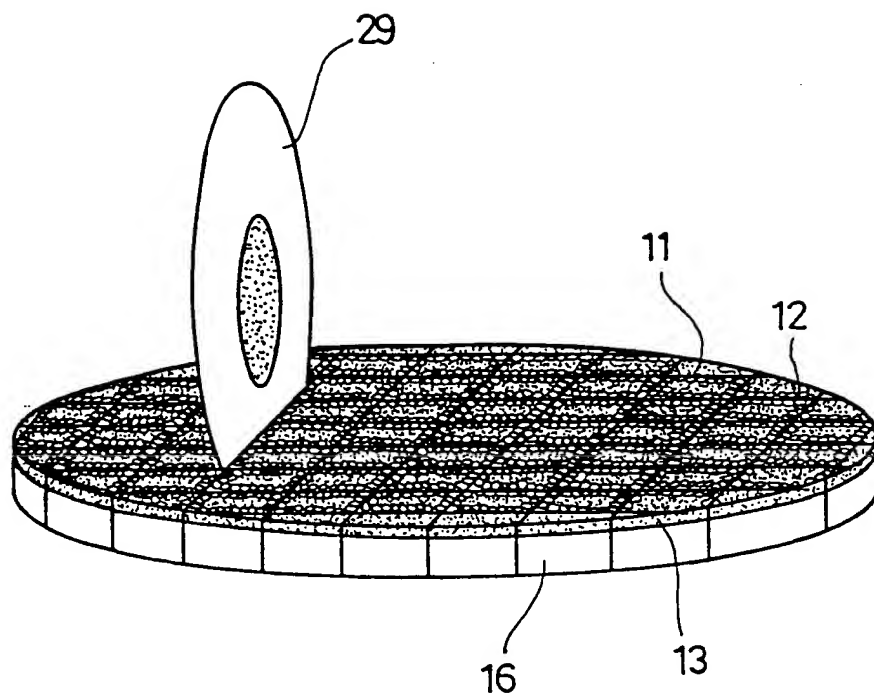
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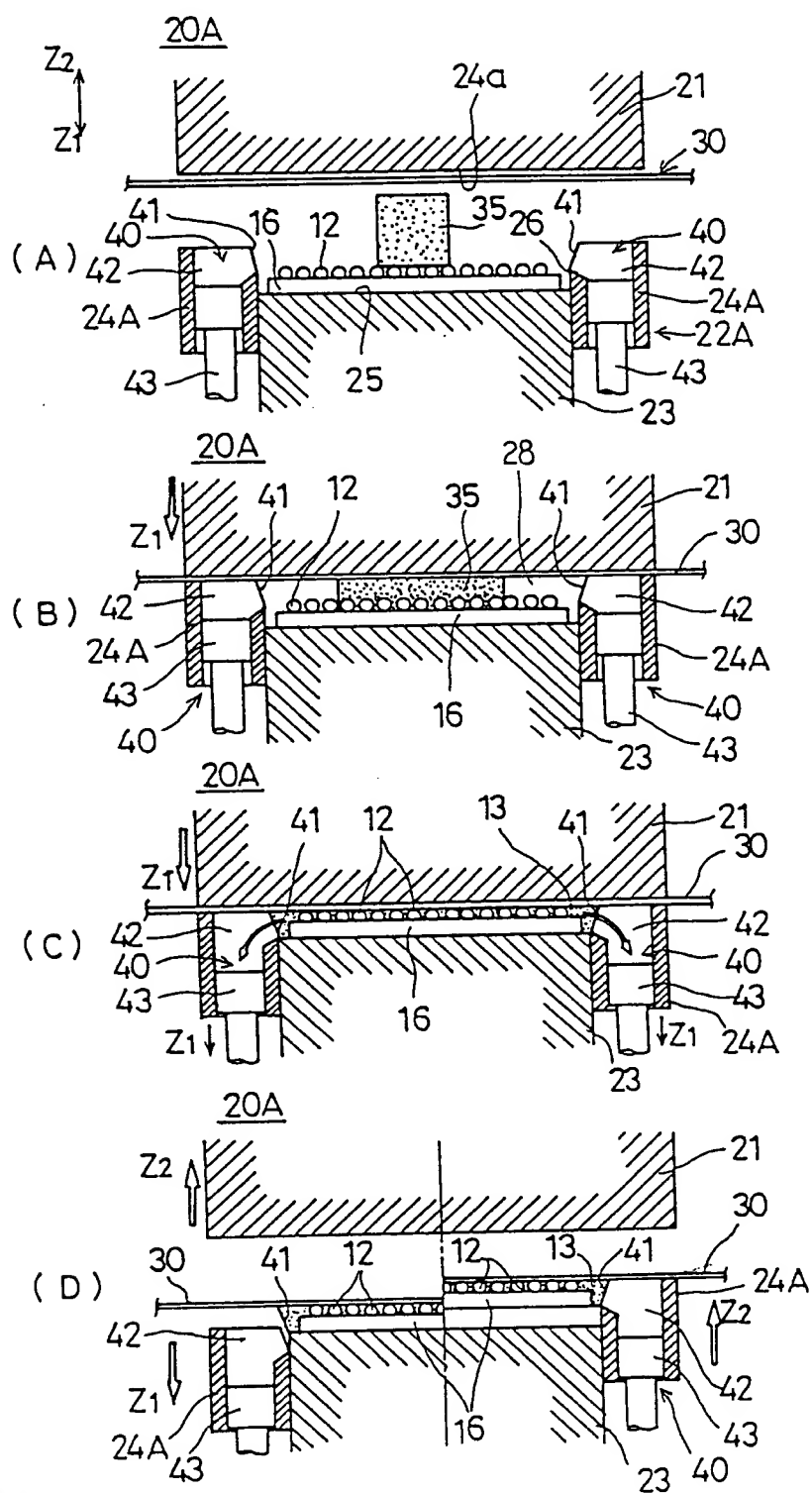


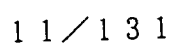
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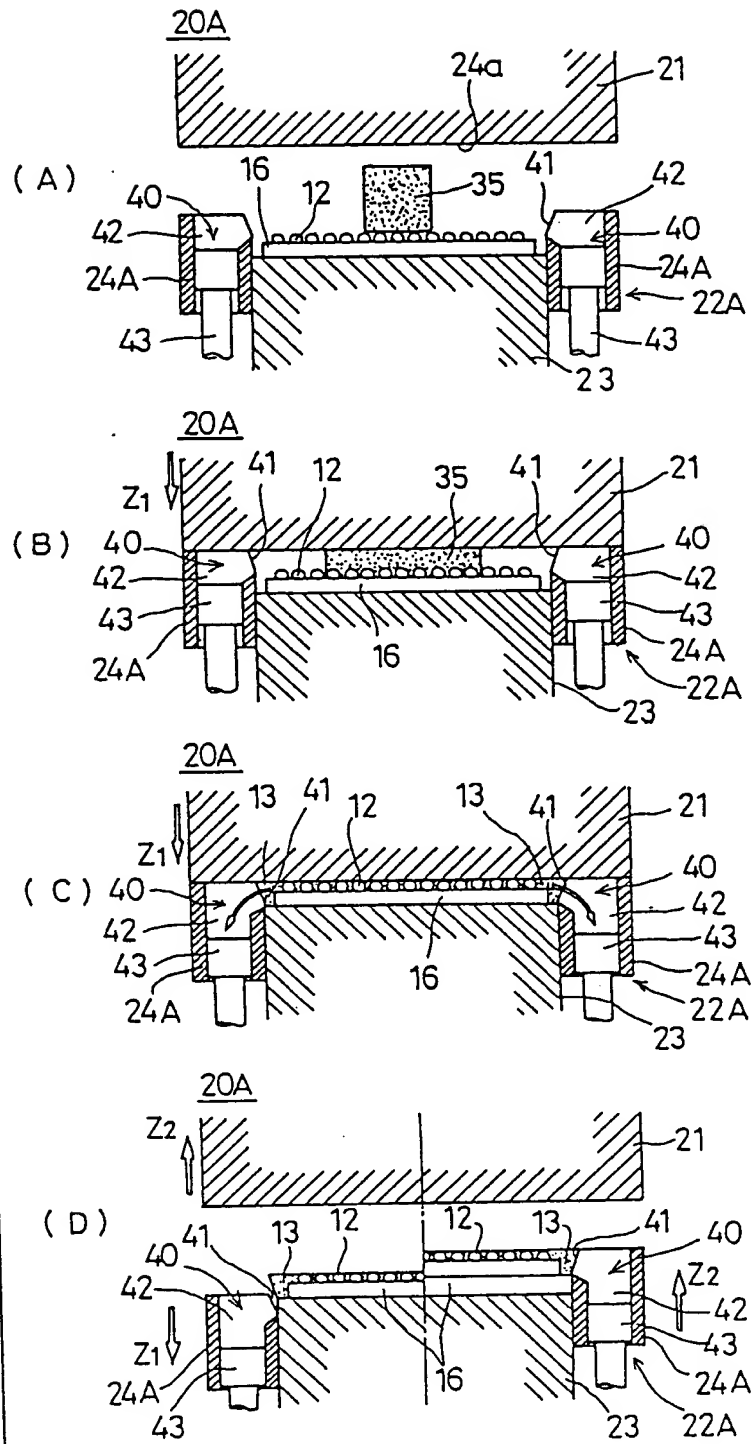
☒ 10





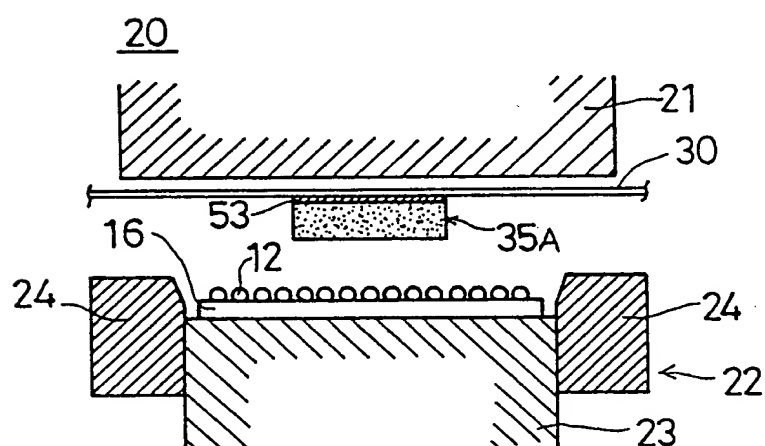
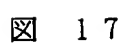
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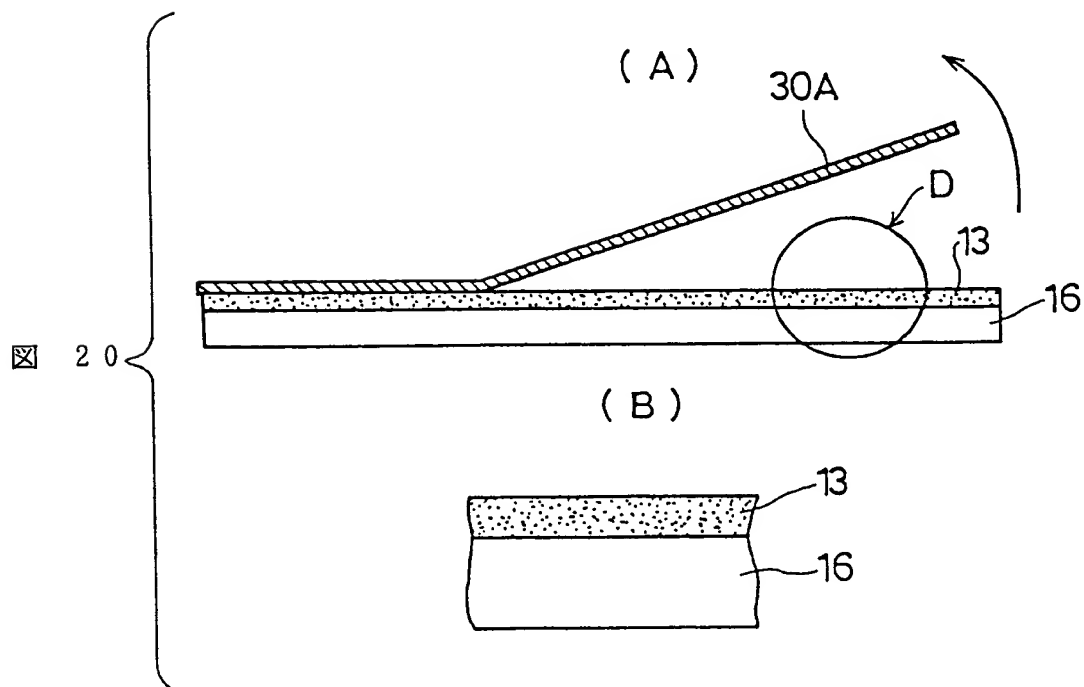
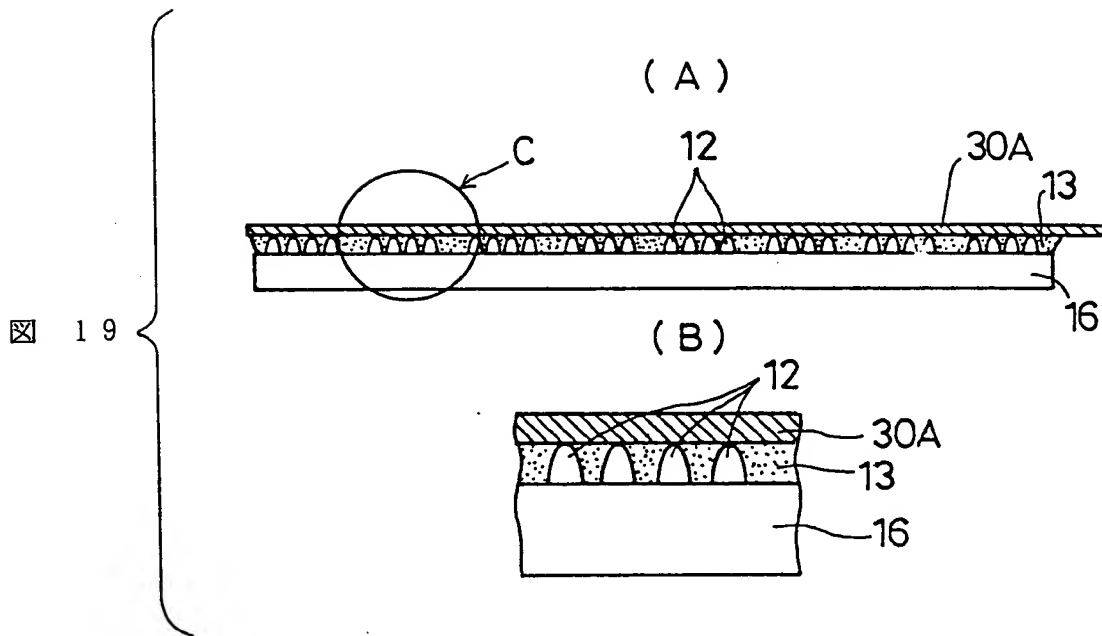
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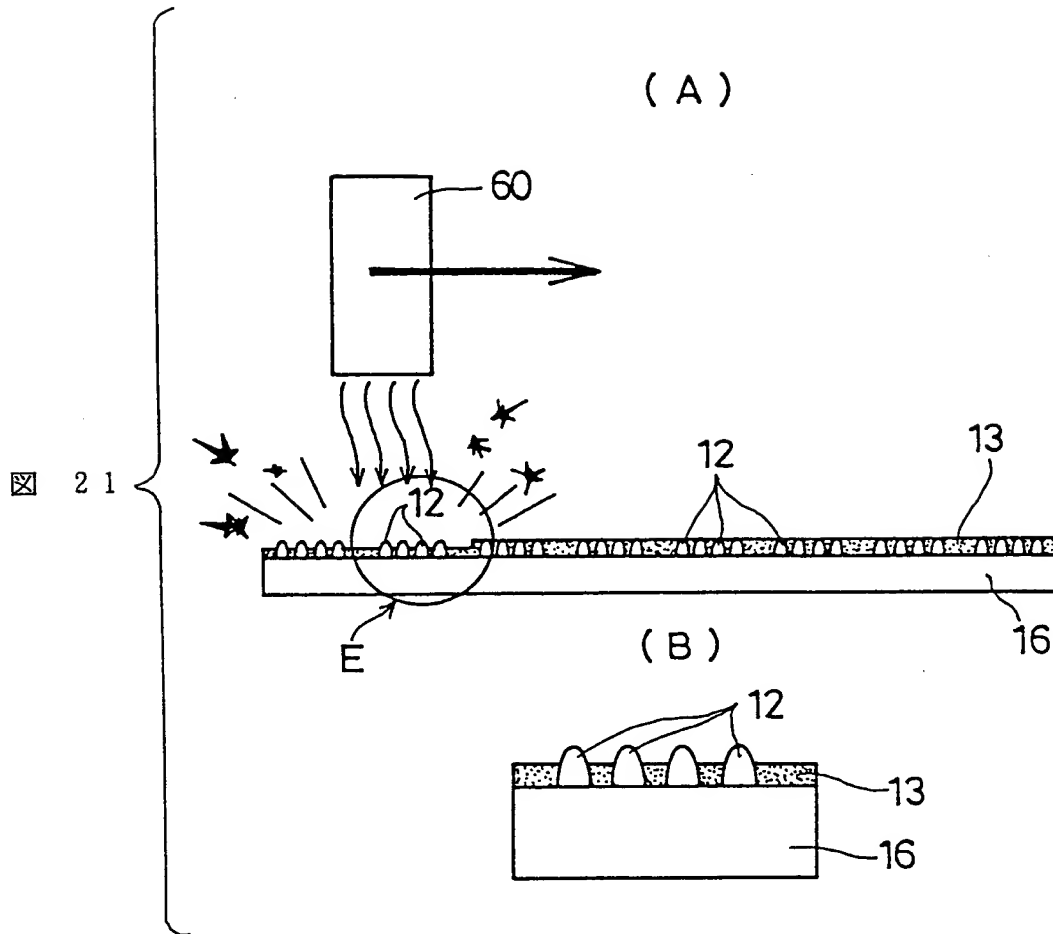




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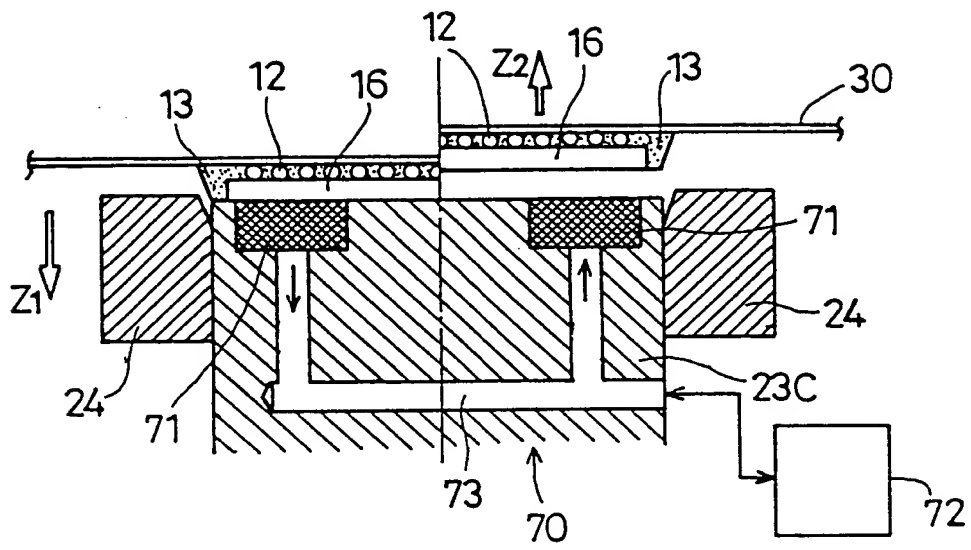






☒ 22

20C



[illegible]

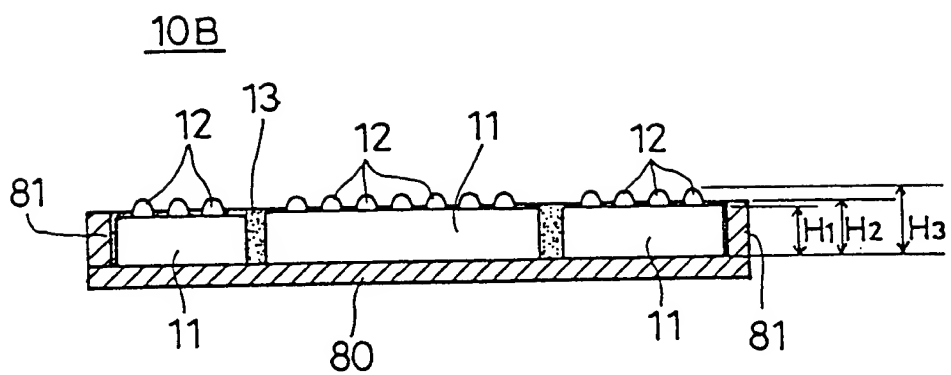
Figure 20E is a cross-sectional view of a semiconductor device 20E. The device features a substrate 23 with a central region 16. A layer 12 is formed on the substrate, with a patterned layer 13 on top. A layer 74 is formed on the layer 12. A layer 30 is formed on the layer 74. A layer 24E is formed on the substrate 23, with a layer 24E' on top. A layer 24E'' is formed on the layer 24E'. A layer 24E''' is formed on the layer 24E''. A layer 24E'''' is formed on the layer 24E'''. A layer 24E''''' is formed on the layer 24E''''.

[illegible]

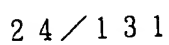
This diagram shows a cross-sectional view of a second embodiment of the device. It features a central horizontal member 75. On the left, member 75 is connected to a vertical support structure 21F. On the right, member 75 is connected to a vertical support structure 23F. A component 24F is positioned between the right end of member 75 and the support structure 23F. An arrow points to the interface between 24F and 23F, labeled 22F. The entire assembly is shown in cross-section with diagonal hatching.

Figure 10A is a cross-sectional view of a semiconductor device 10A. The device consists of a substrate 80 with a patterned layer 11 on top. The layer 11 has three rectangular regions separated by narrow gaps. The top surface of the layer 11 is covered with a series of small, rounded protrusions 12. A vertical line 13 is shown on the right side of the device, indicating a cross-section through the structure.

27

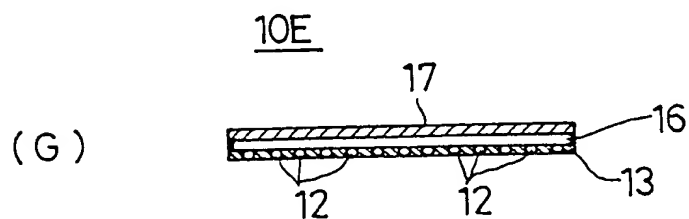
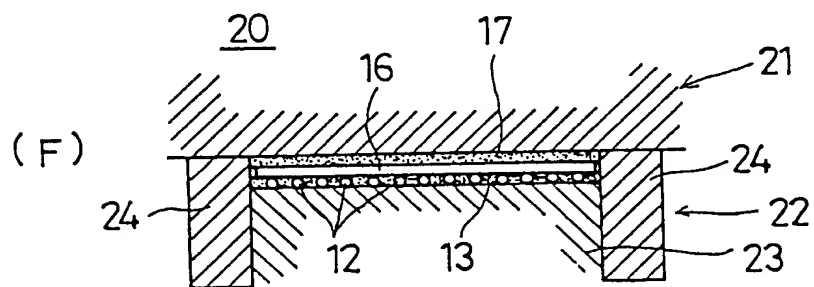
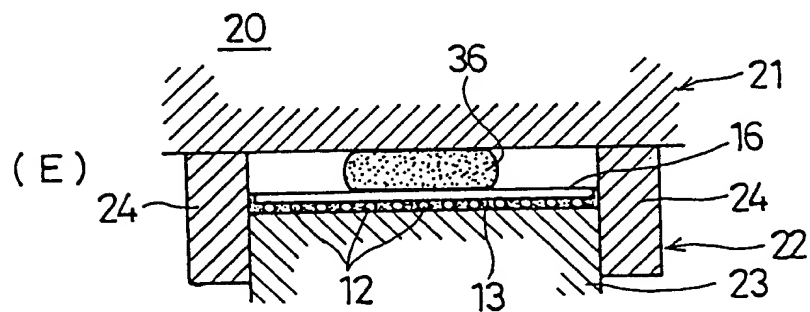
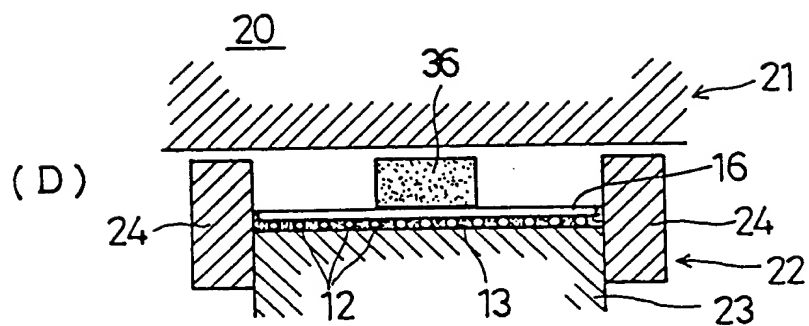






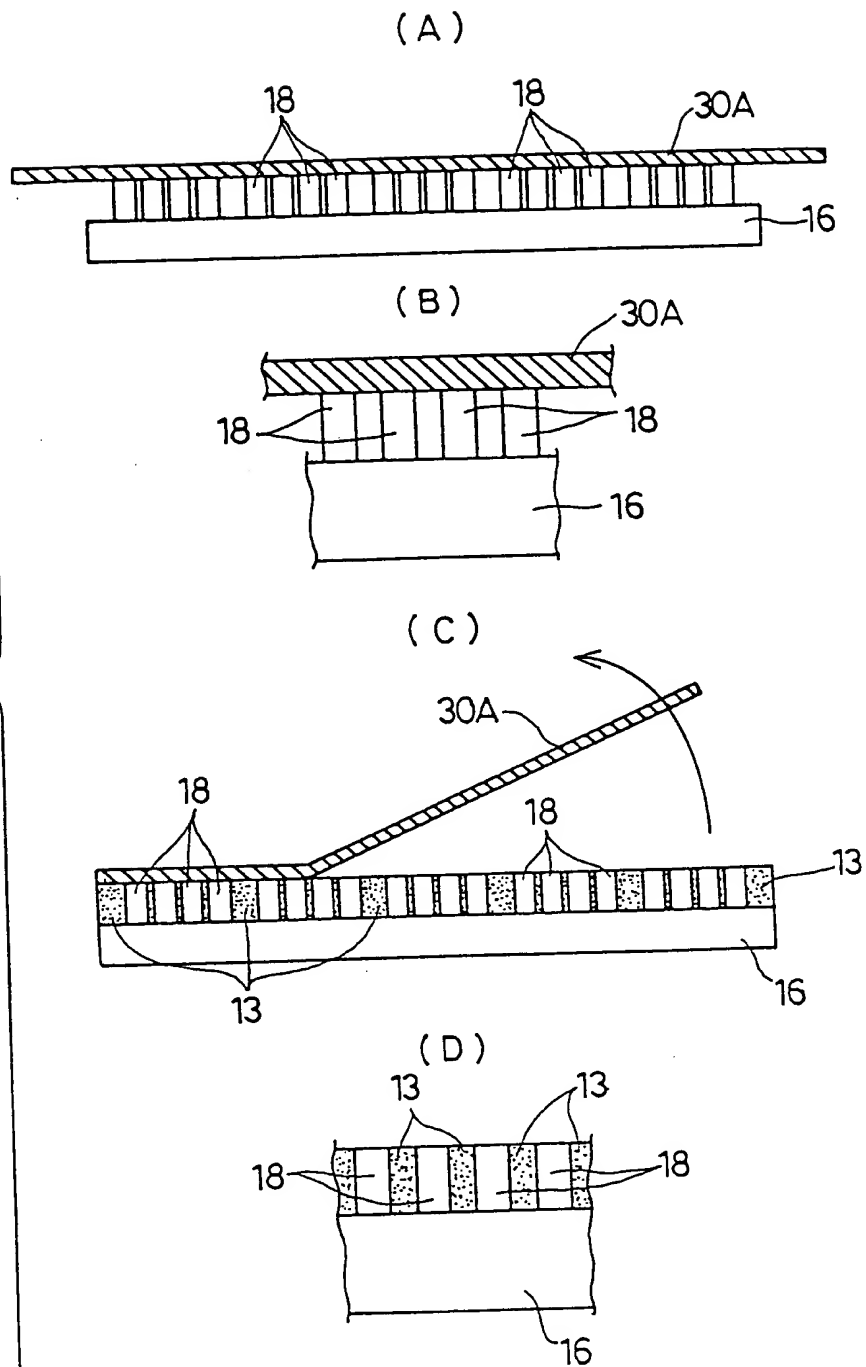
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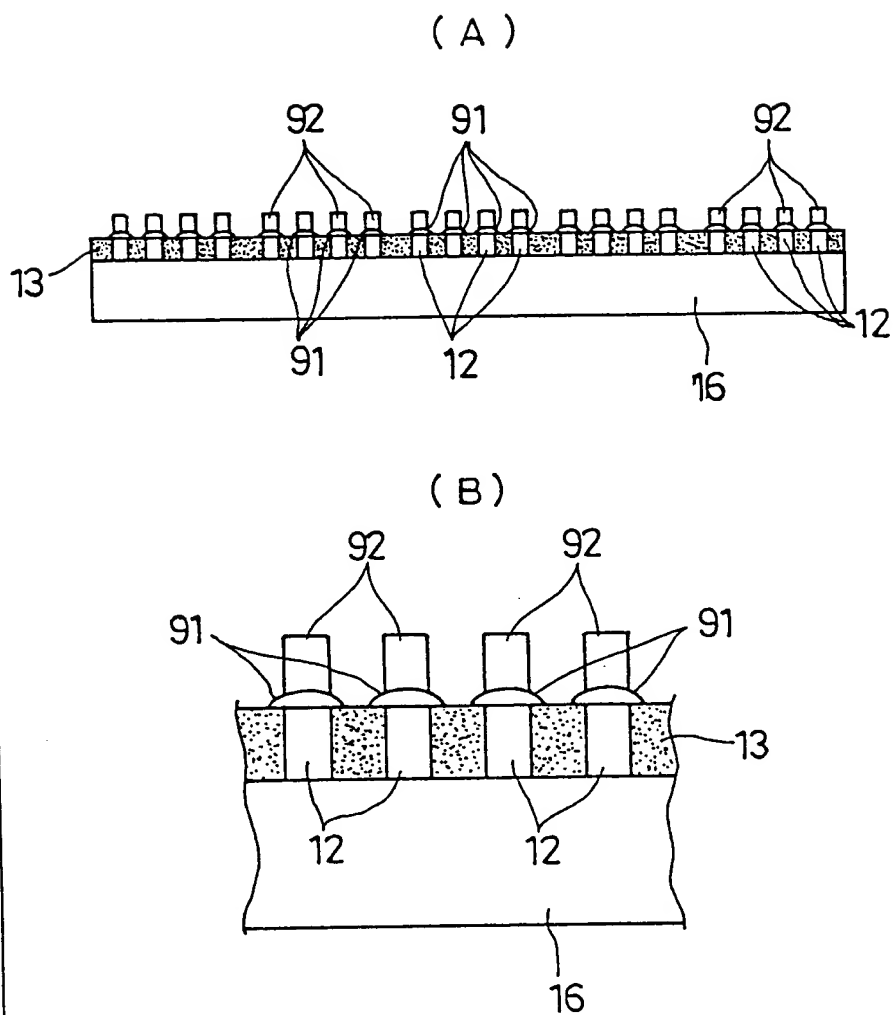


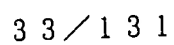
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37





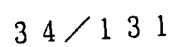
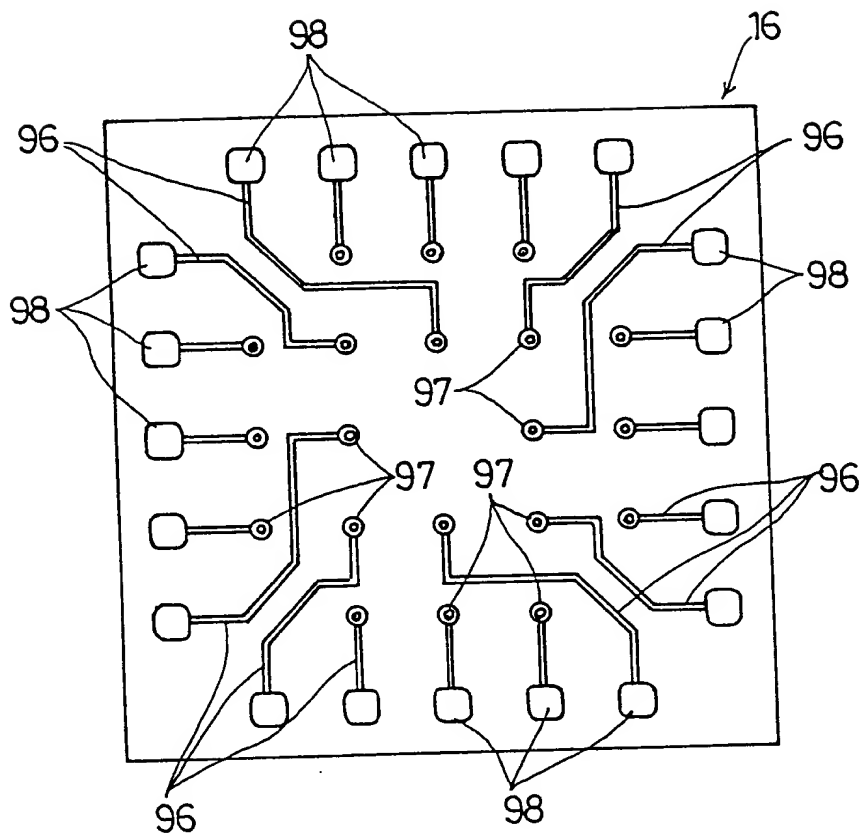
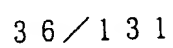
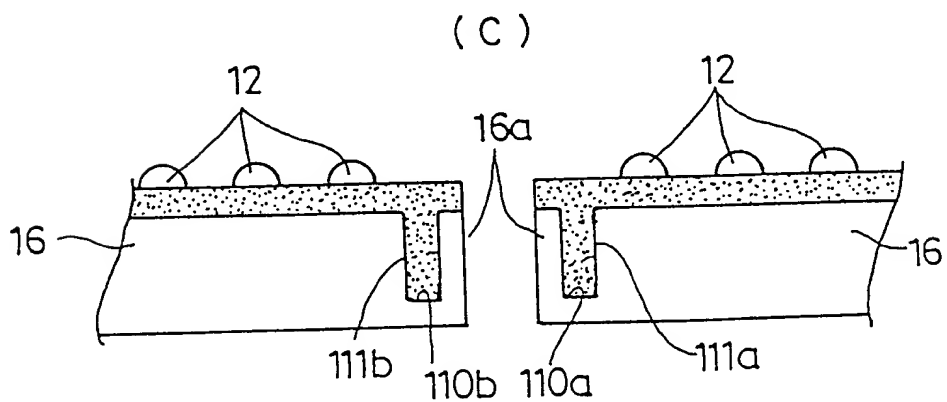
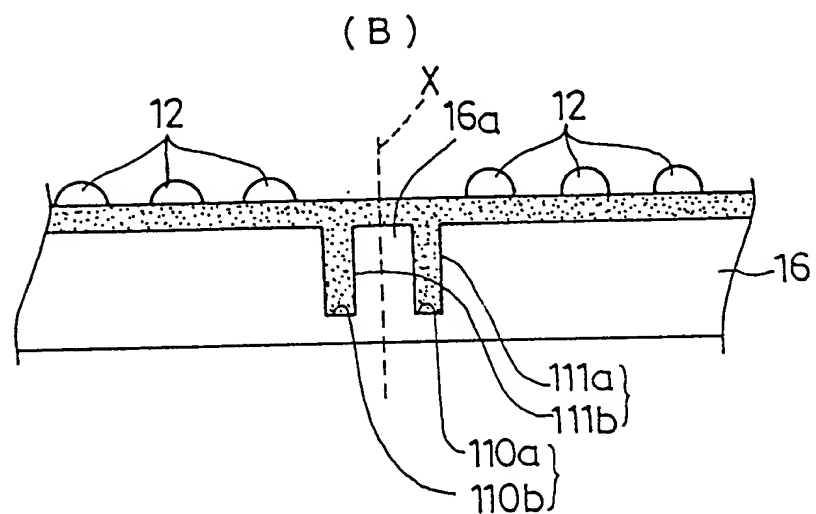
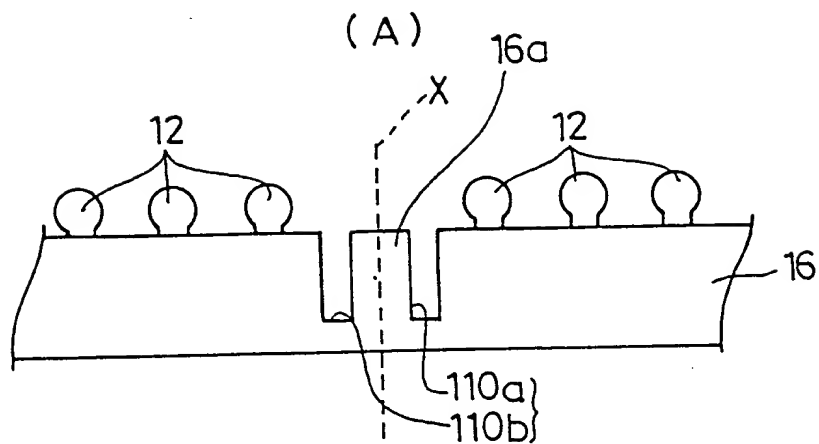


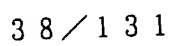
图 40

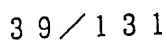






42



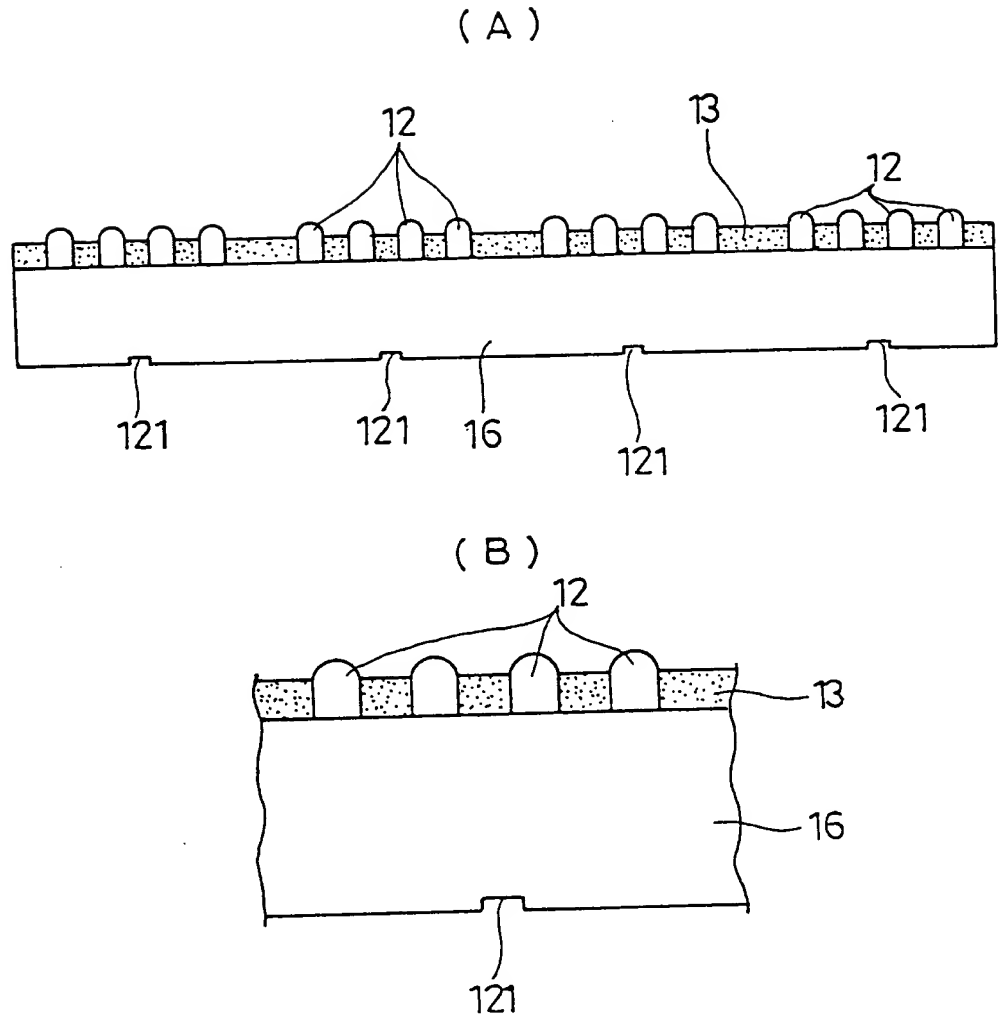


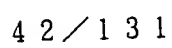
[illegible]10F

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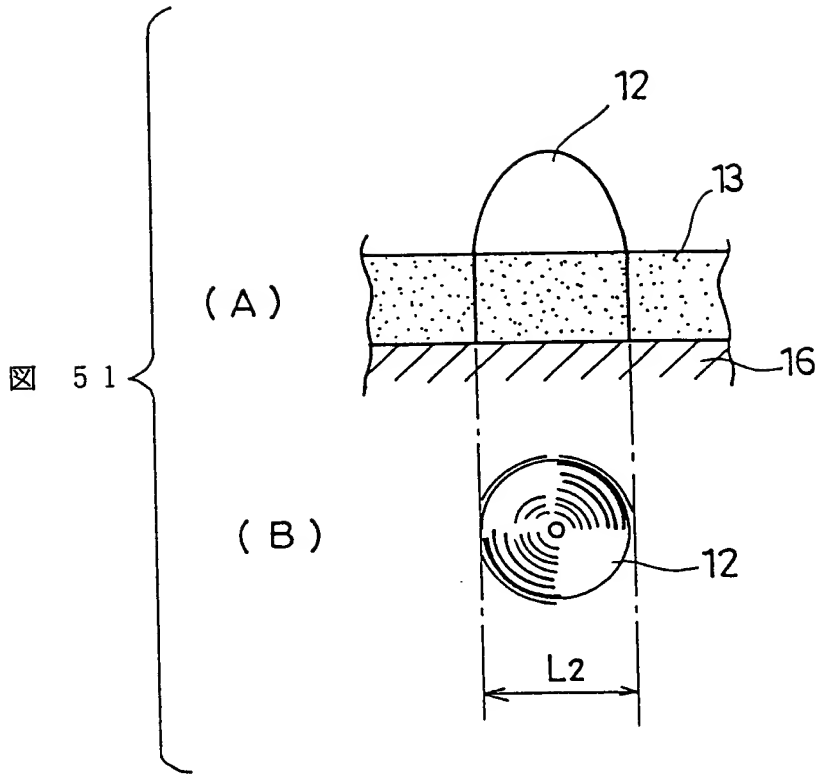
47





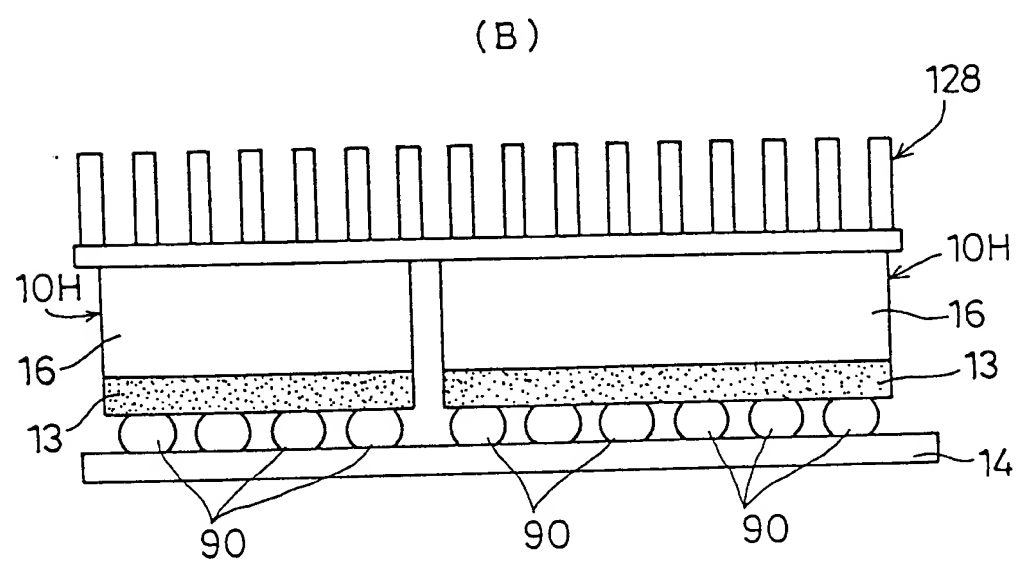
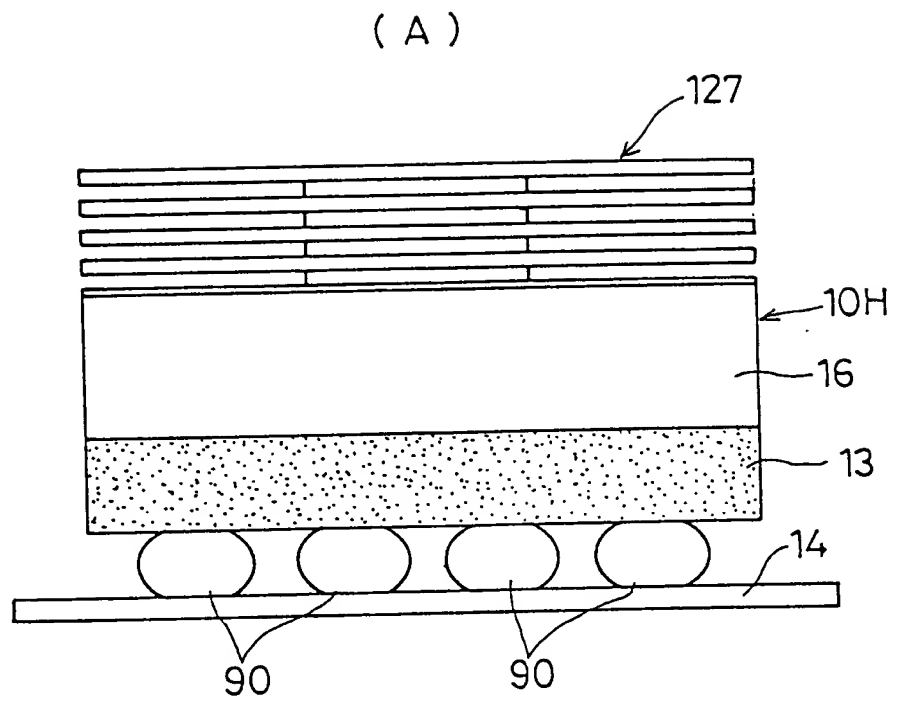


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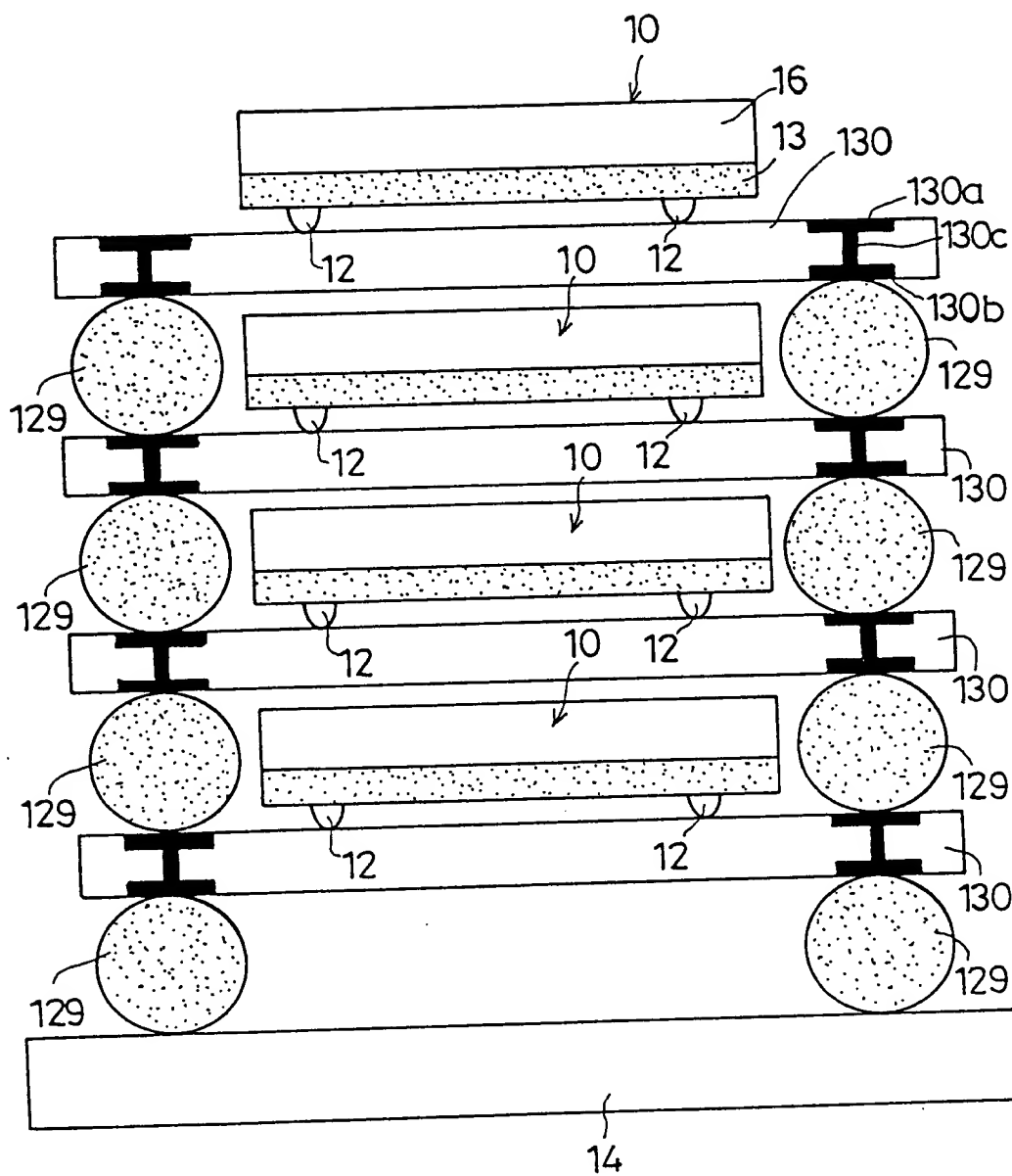


A cross-sectional view of a semiconductor device 10. The device consists of a substrate 14 with a top surface 12. A thin layer 126 is formed on the top surface 12. A layer 13 is formed on top of layer 126. A rectangular block 16 is formed on top of layer 13. An arrow 10 points to the overall device structure.

A cross-sectional view of a semiconductor device 10. The device consists of a substrate 14 with a trapezoidal top surface. A layer 126 is formed on the substrate, containing two circular features 125. A layer 13 is formed on top of layer 126, and a layer 16 is formed on top of layer 13. An arrow 10 points to the top surface of layer 16.



54



☒ 56

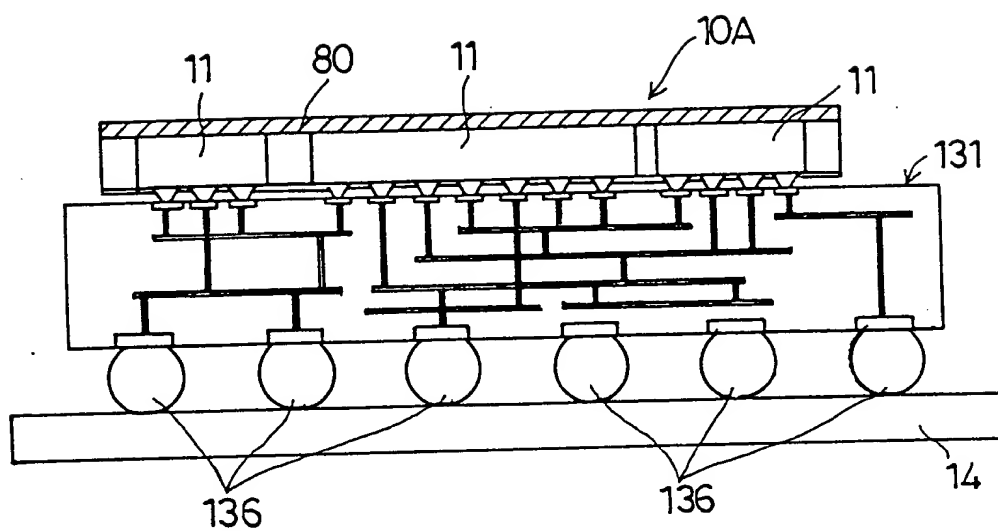
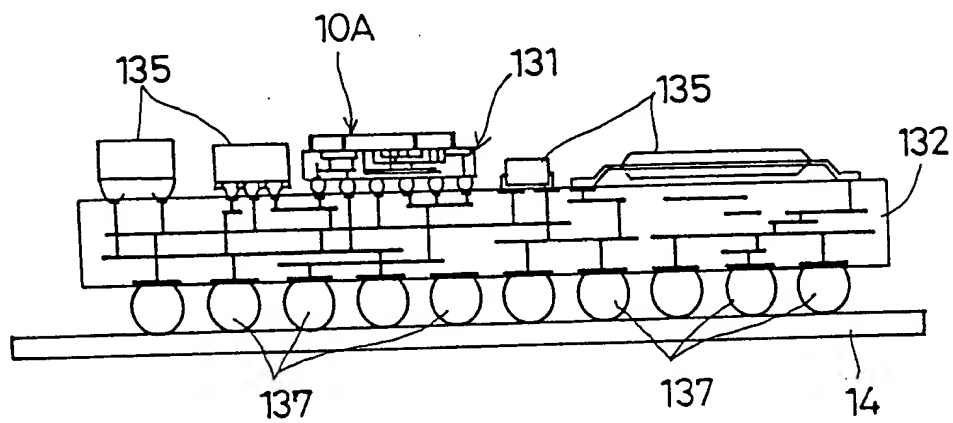
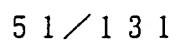
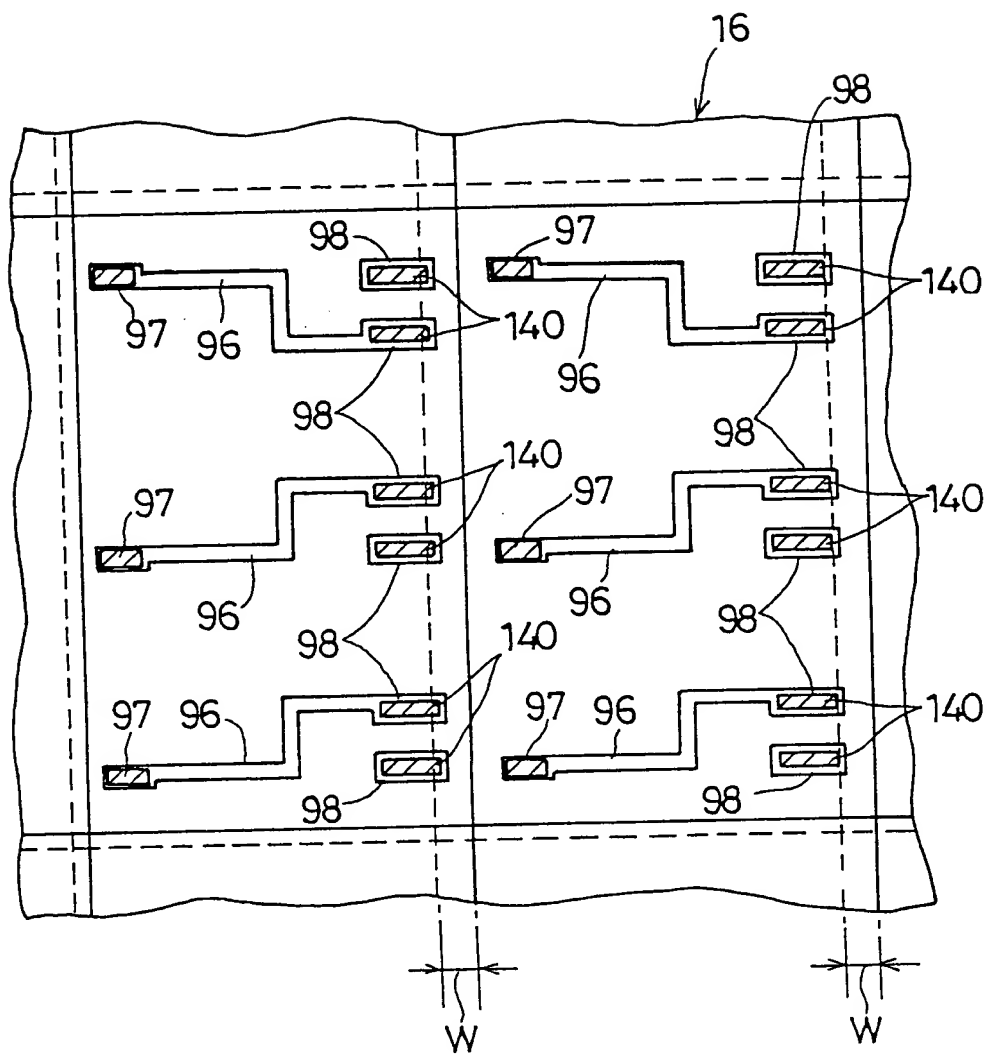


图 57

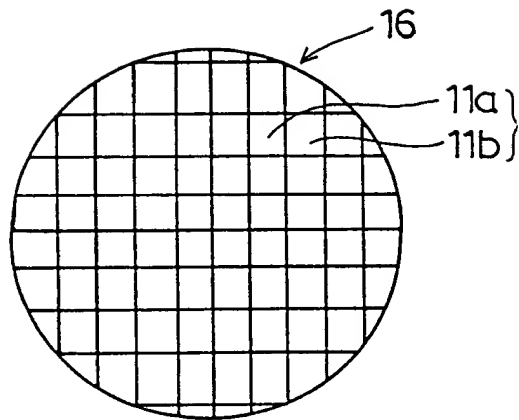


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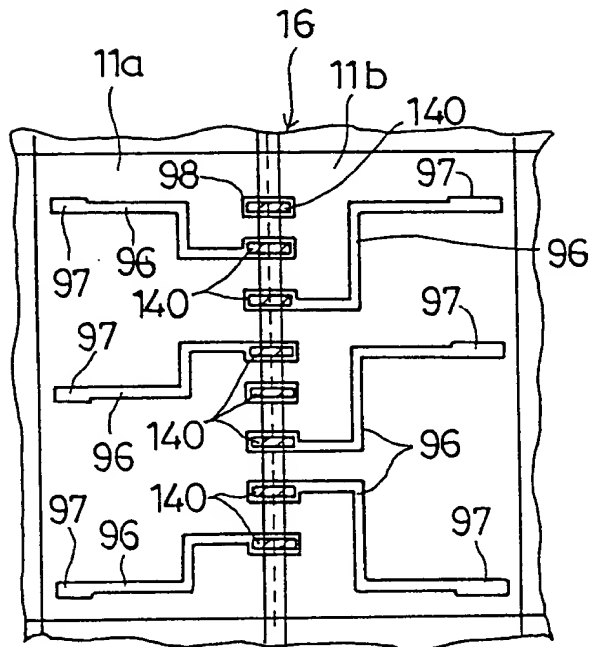


60

(A)



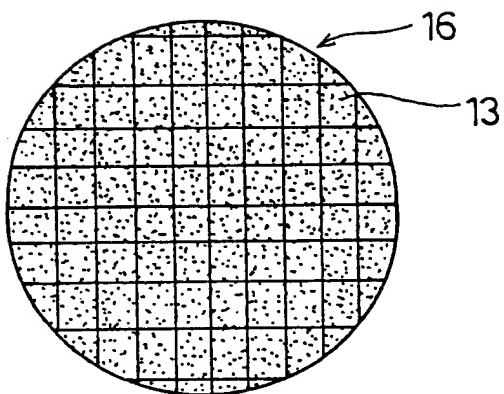
(B)



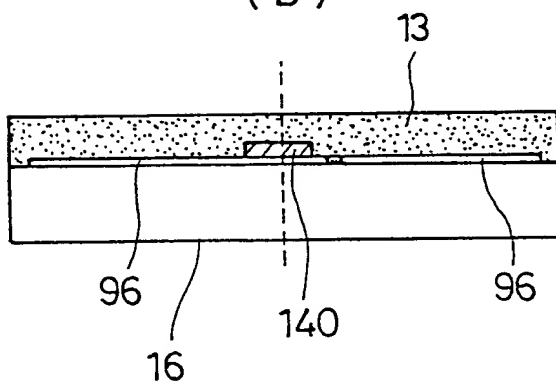
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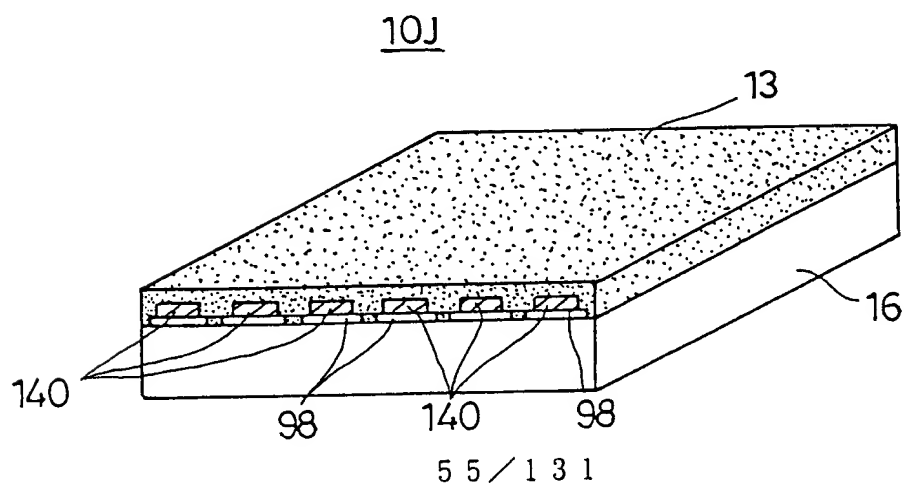
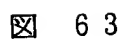
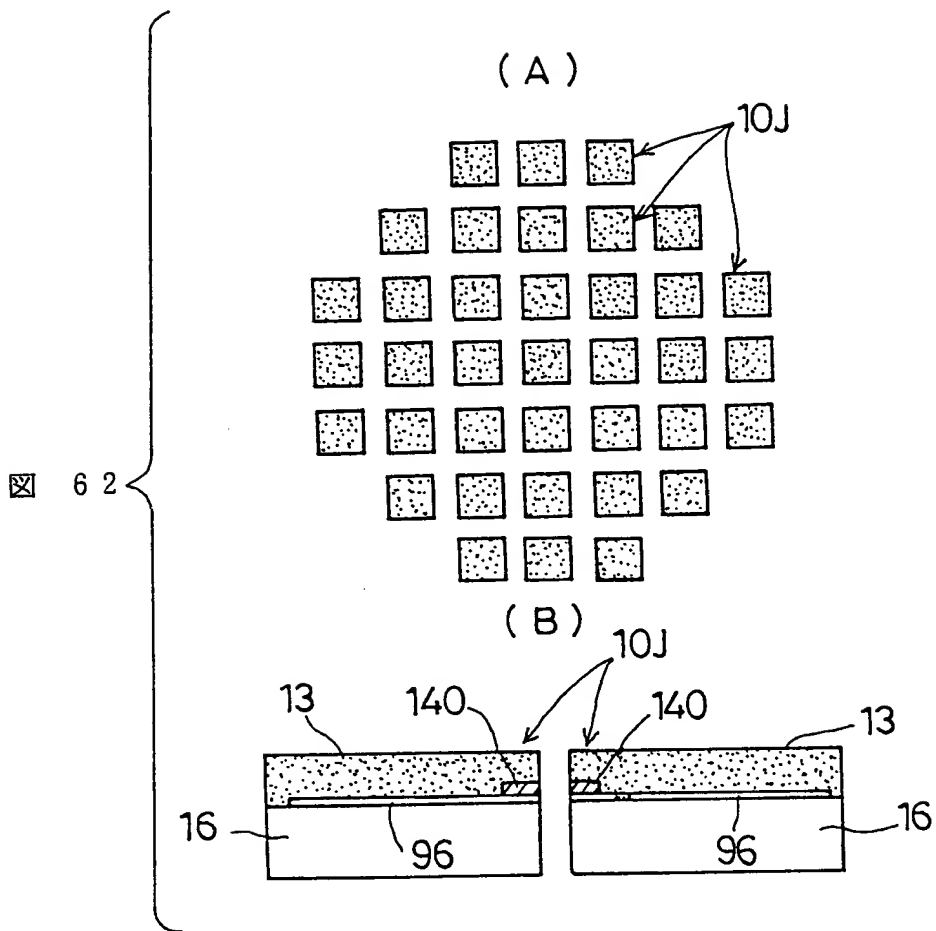
61

(A)

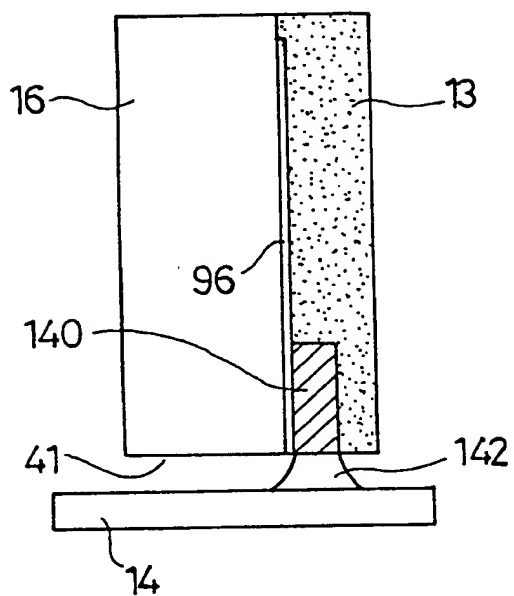


(B)



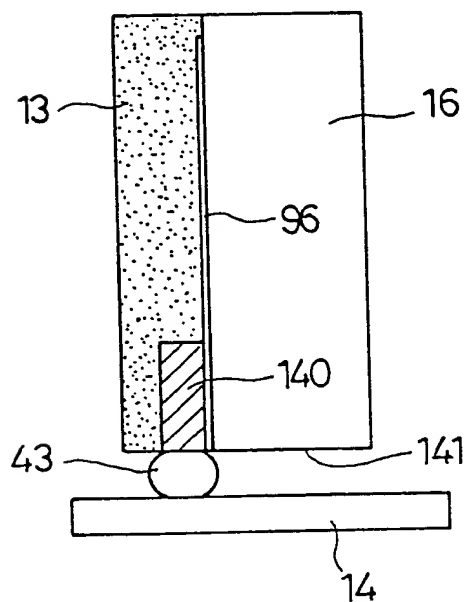


10J



(B)

10J



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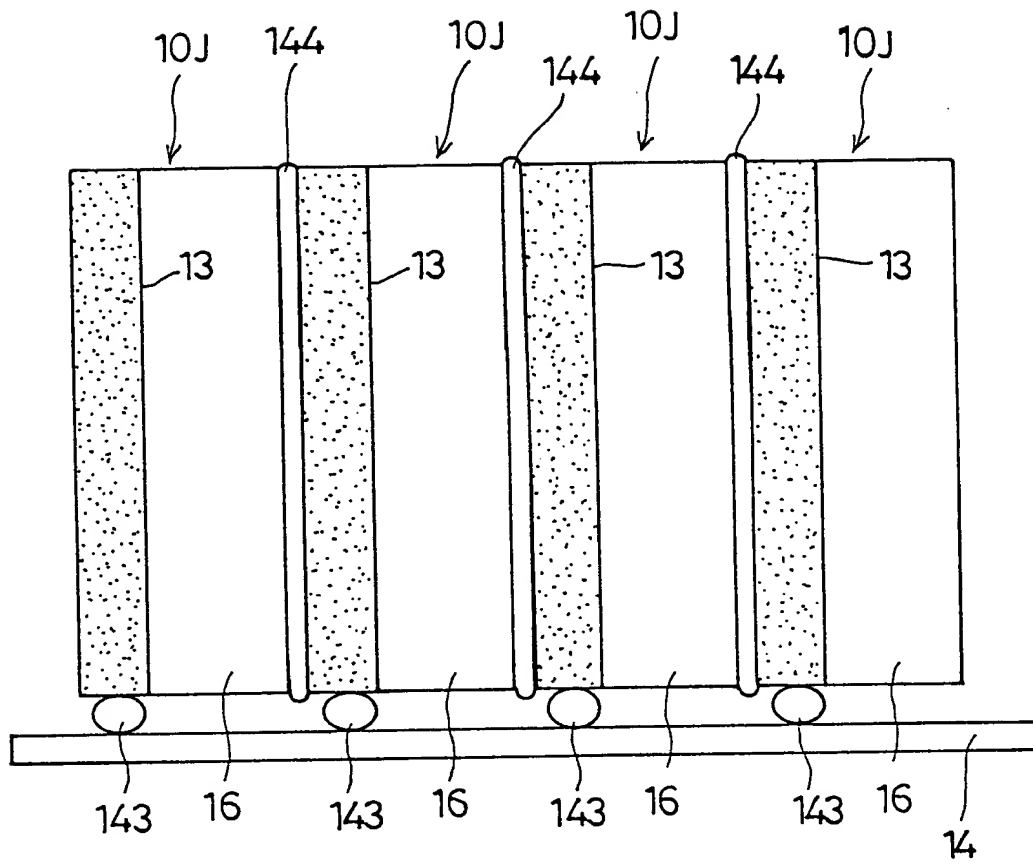
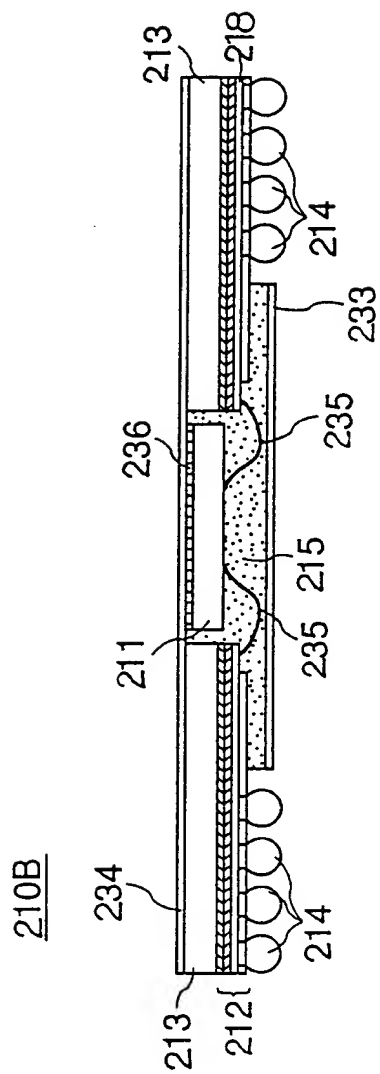
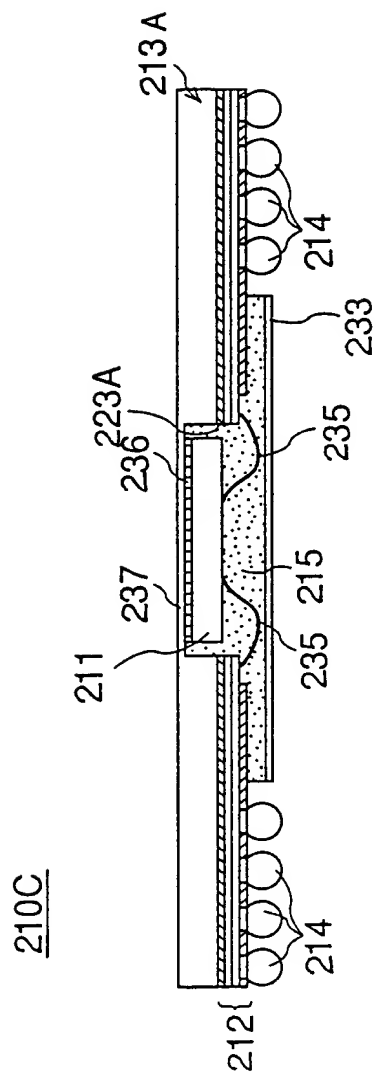


图 84



58 ☒



86

210D

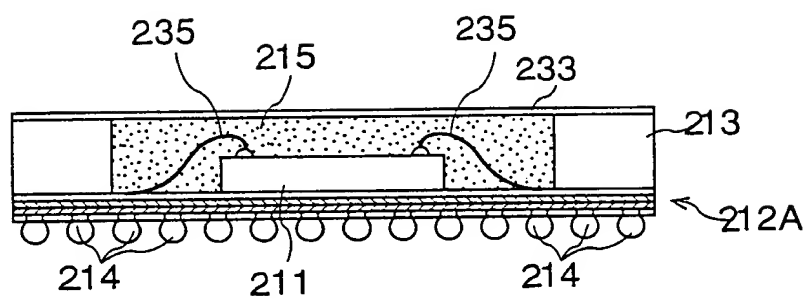
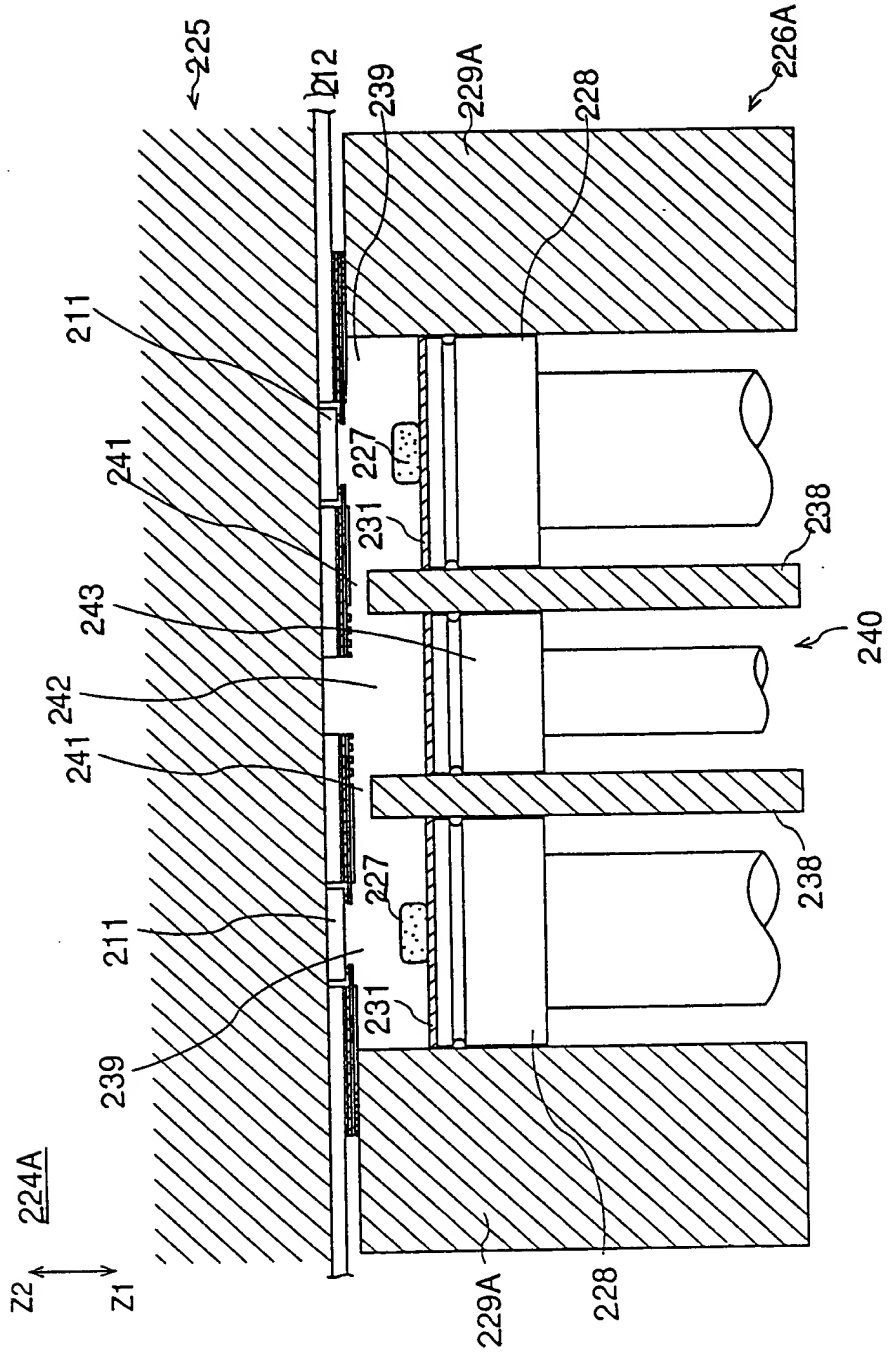
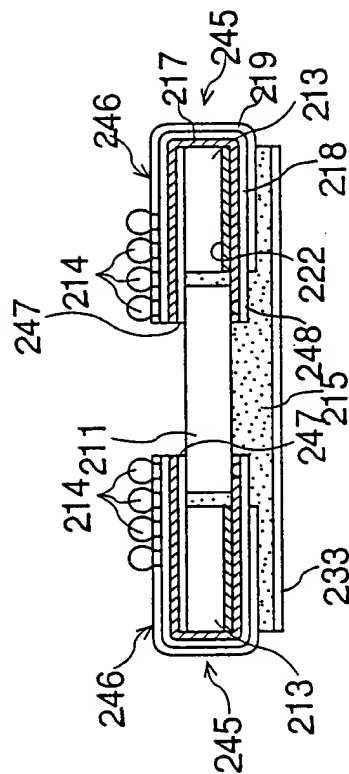


图 87

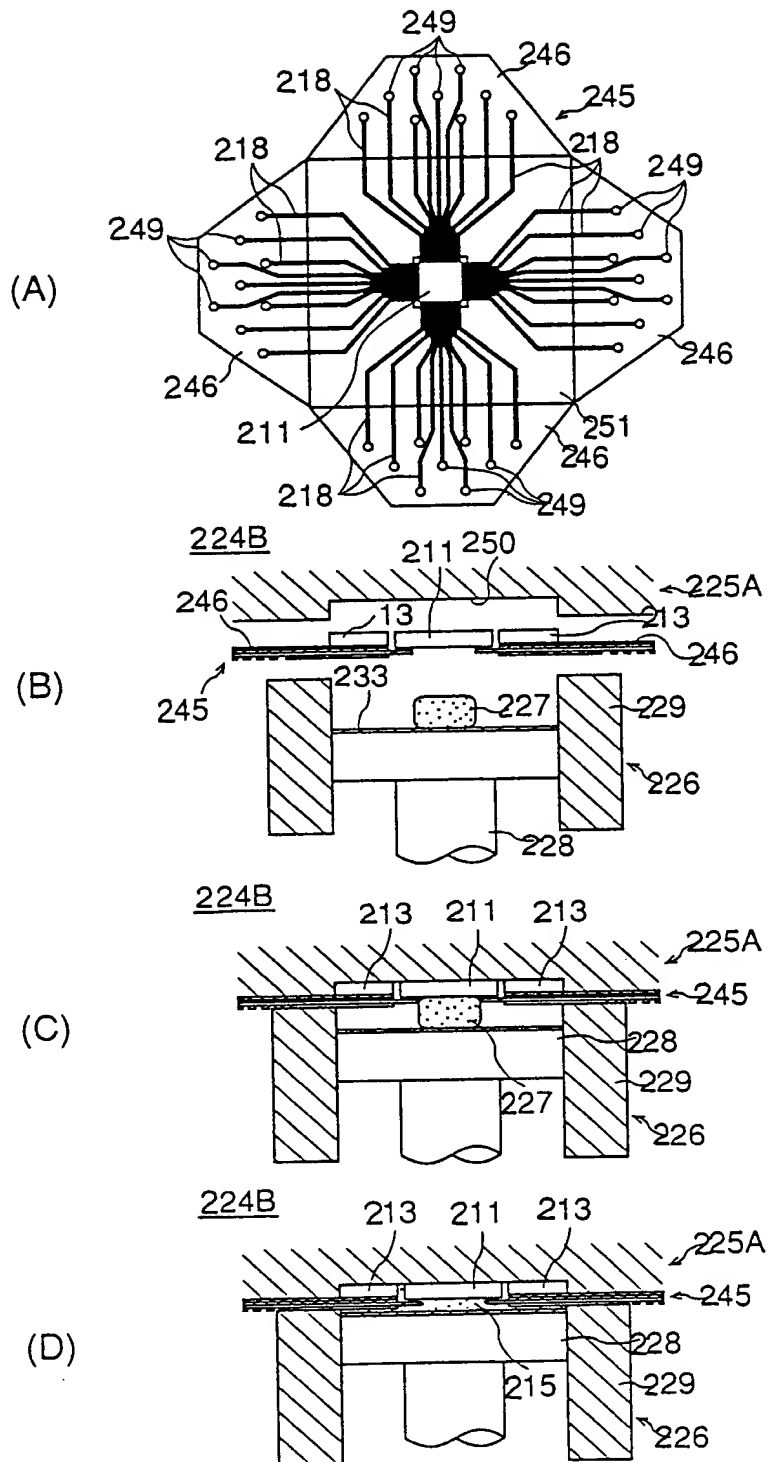


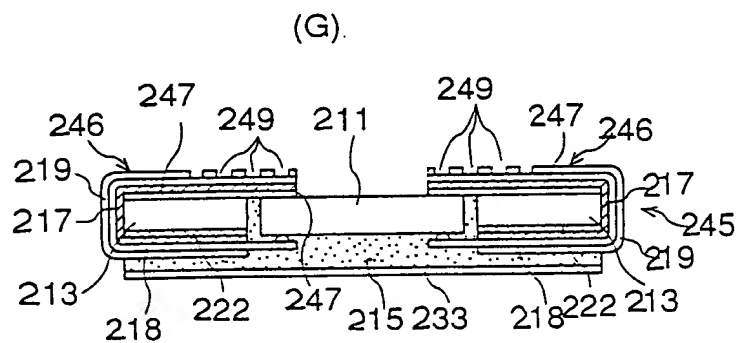
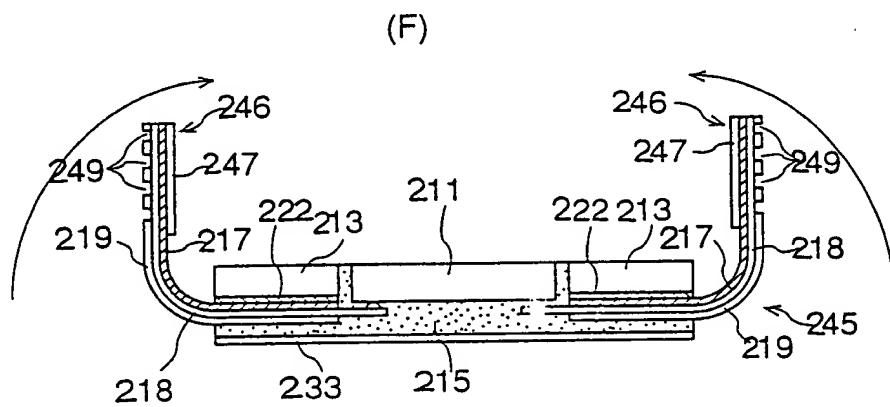
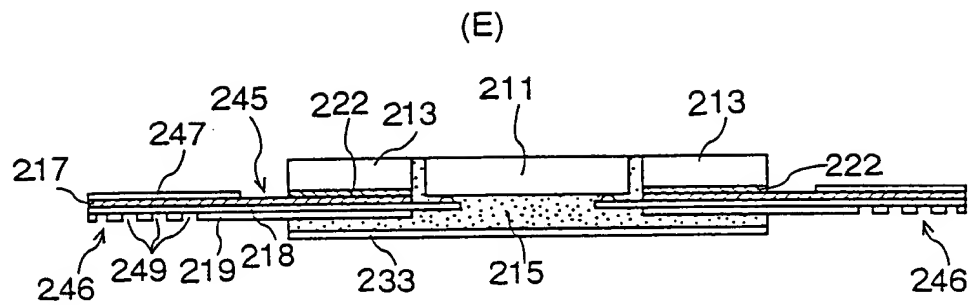
88

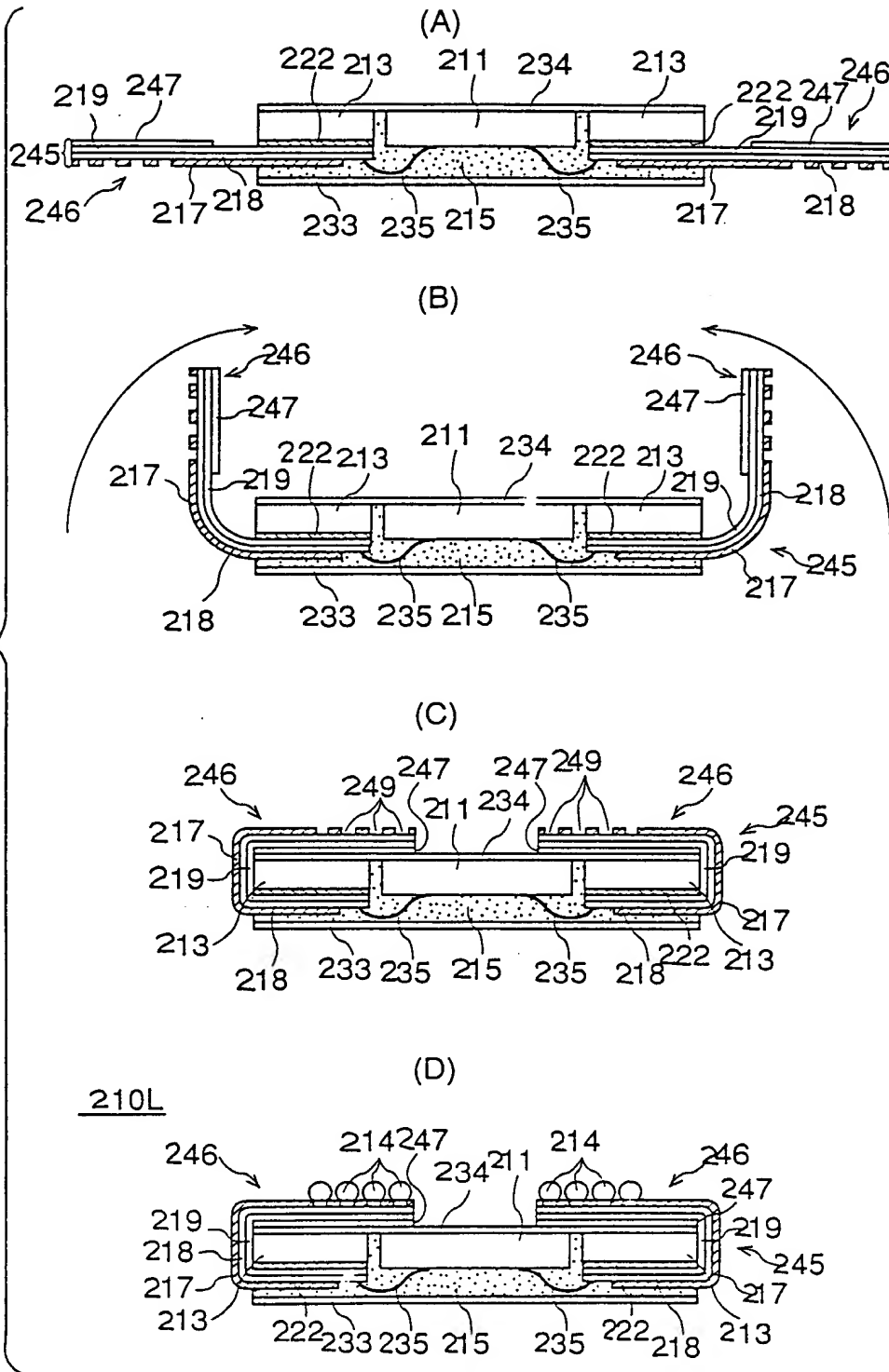
210E



89

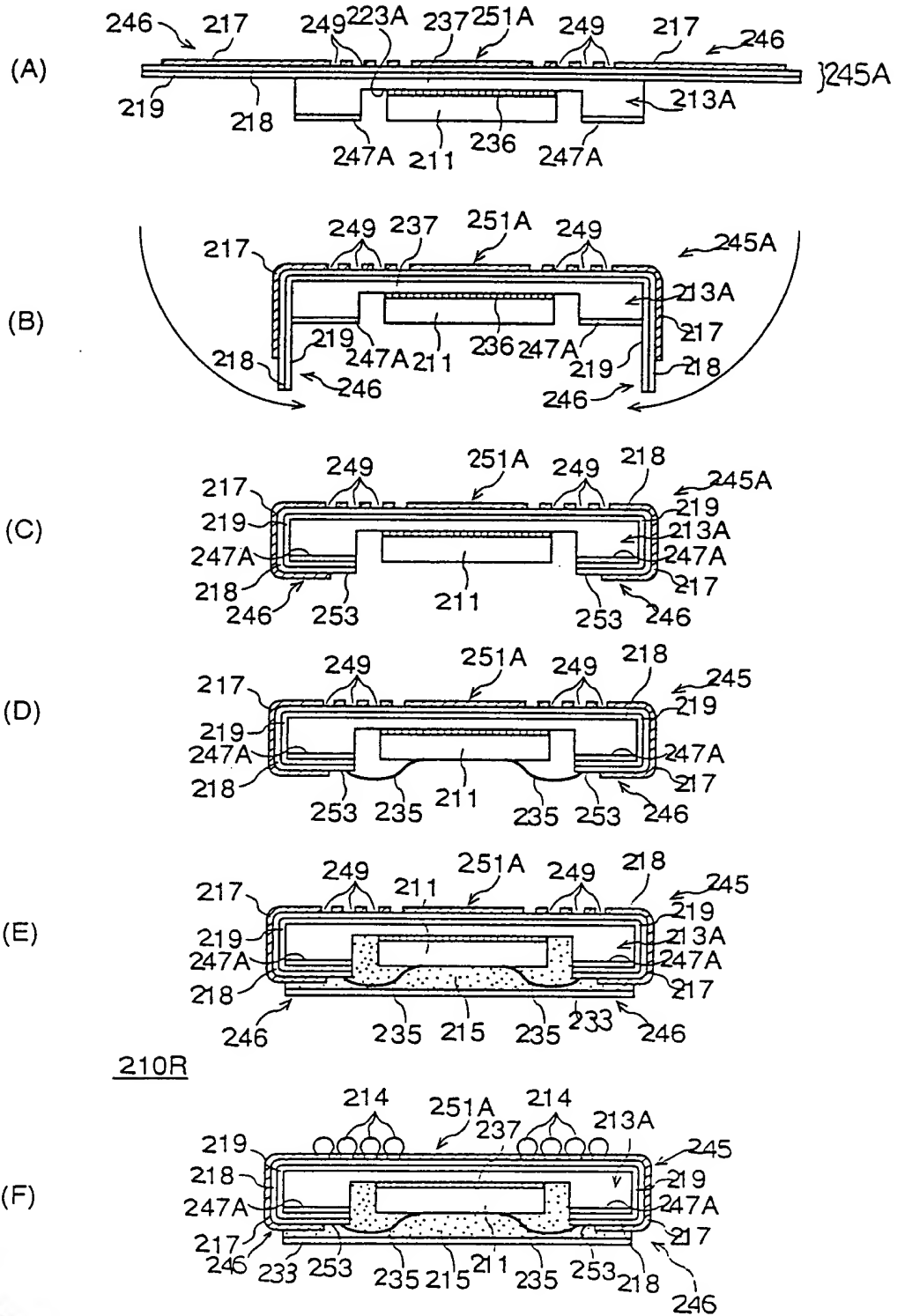




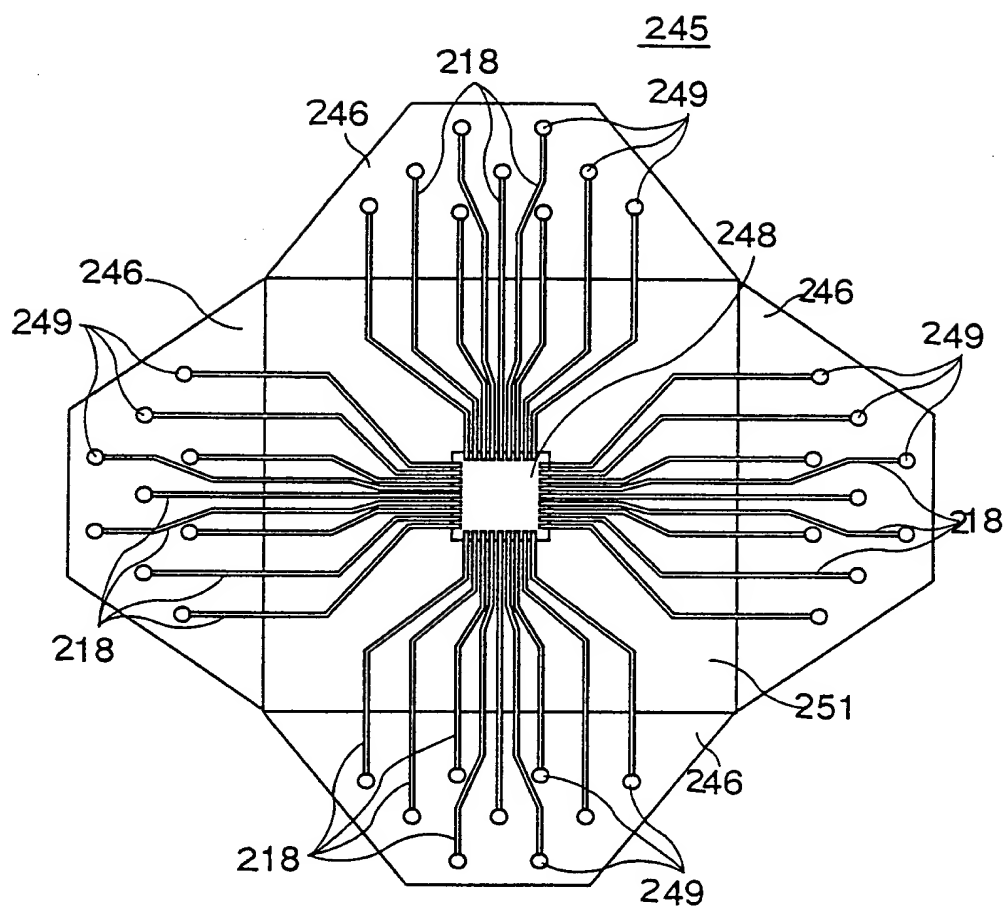


97

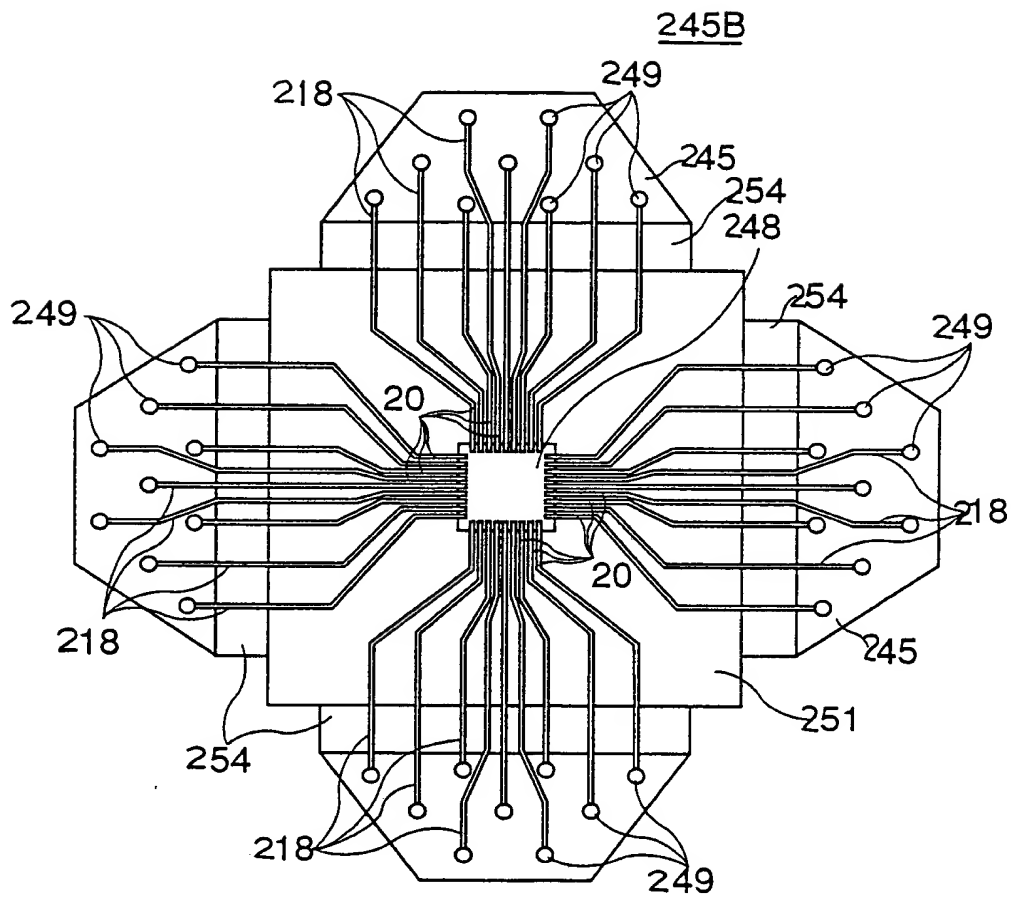
102



103



104



106

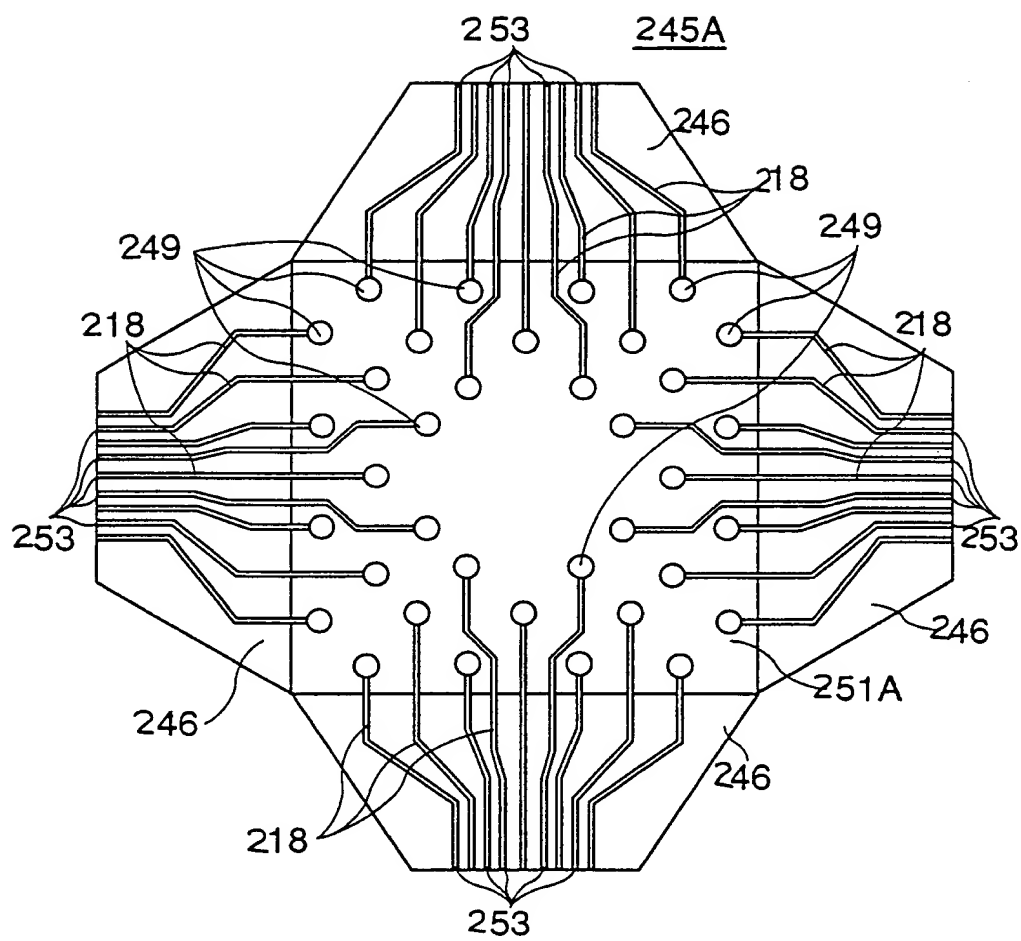
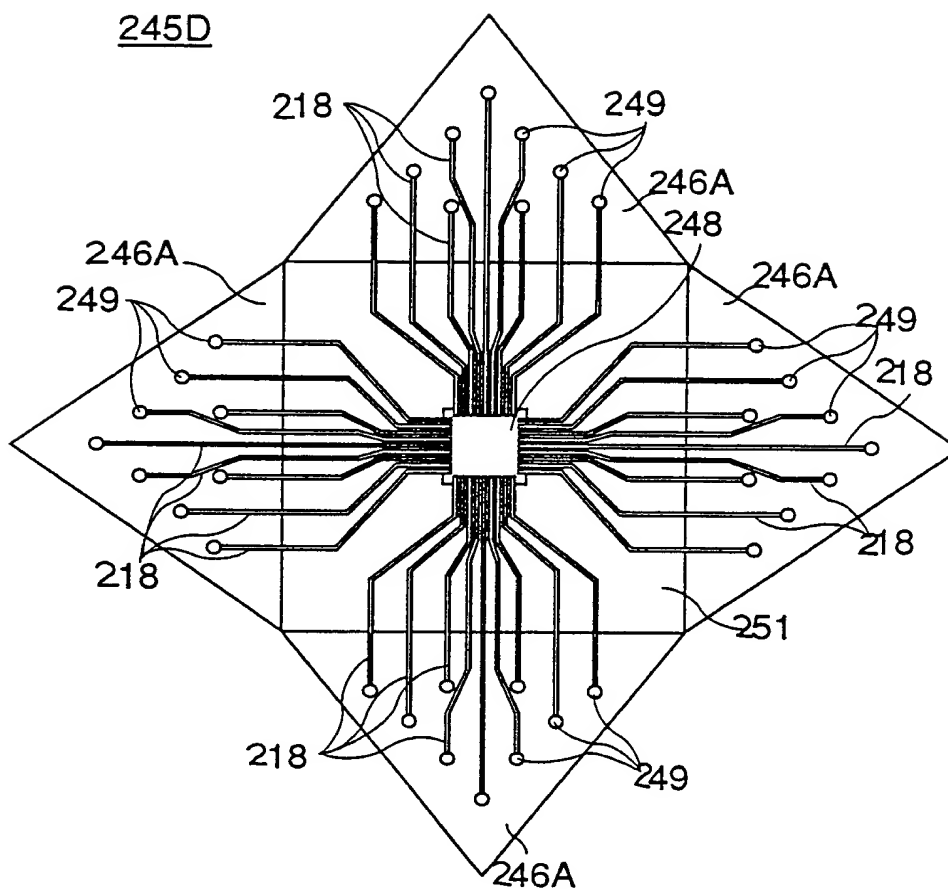
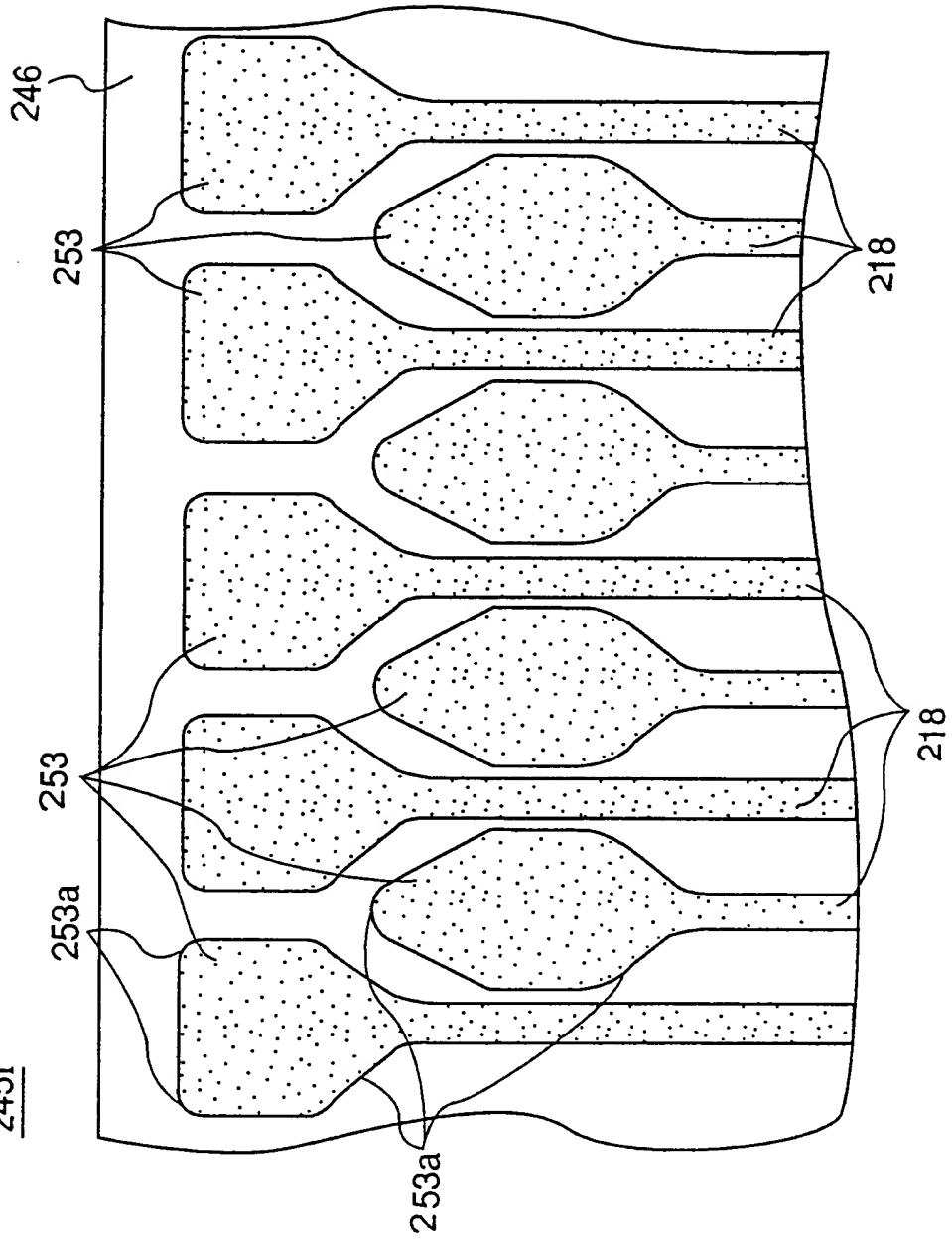


图 107



110

245I



113

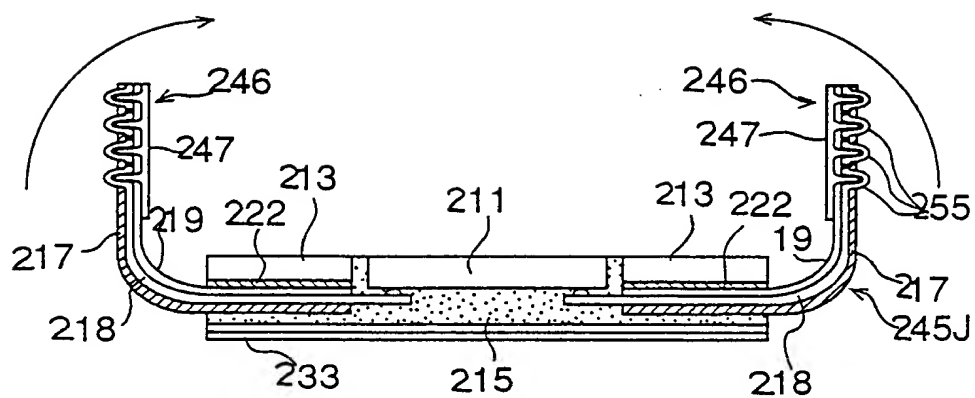


図 118

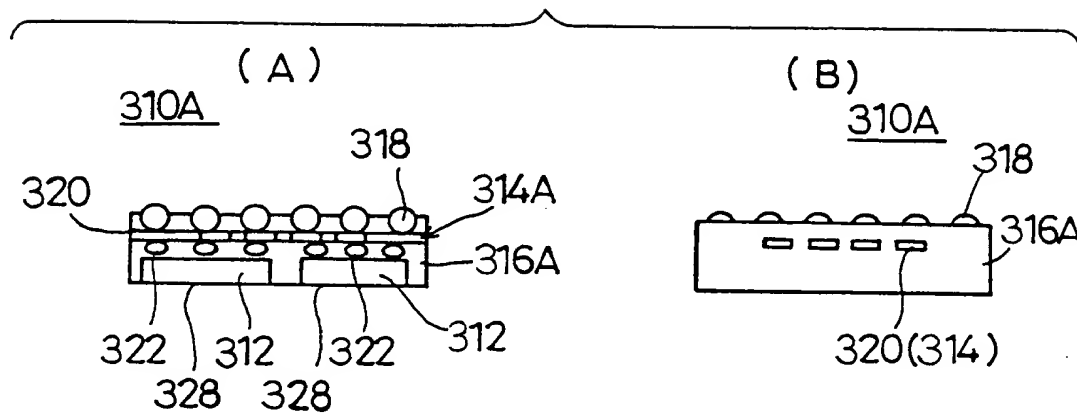
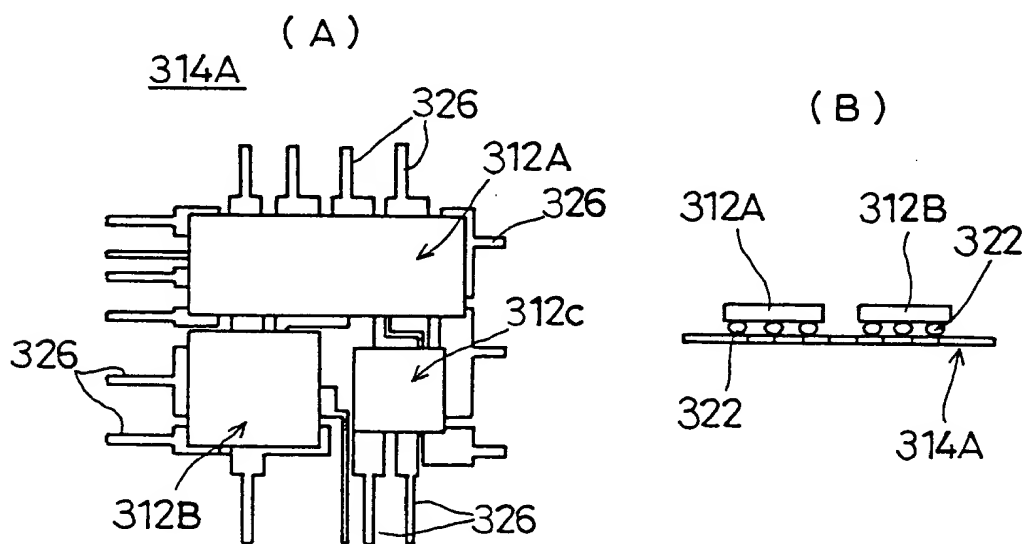


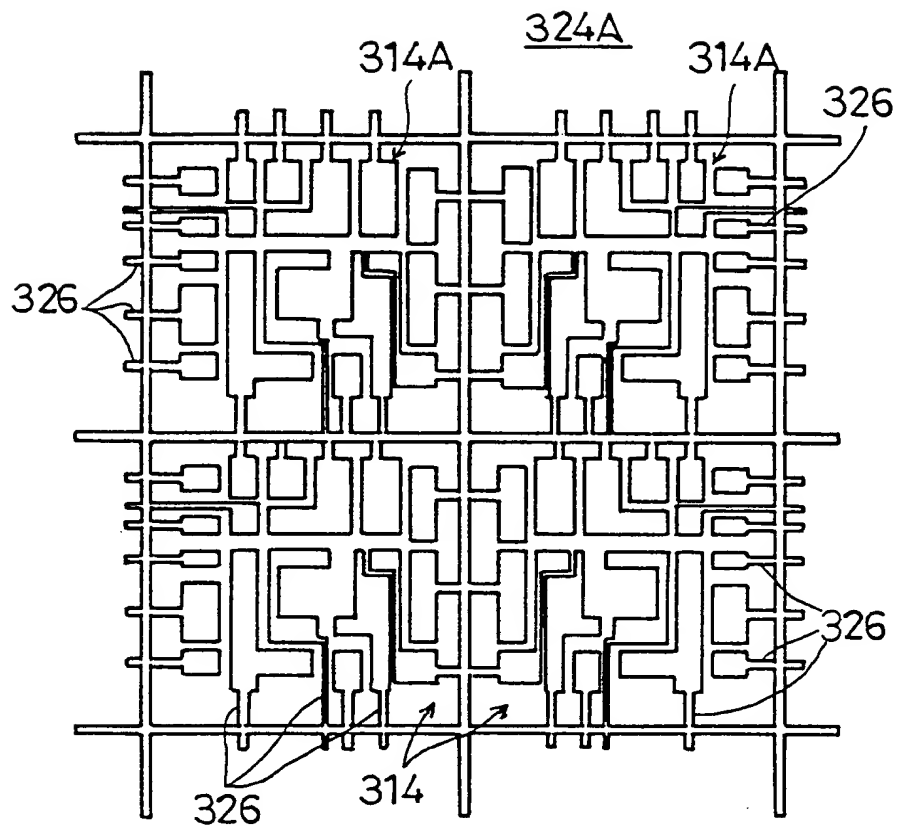
図 120



00000"425E950

119

(A)



(B)

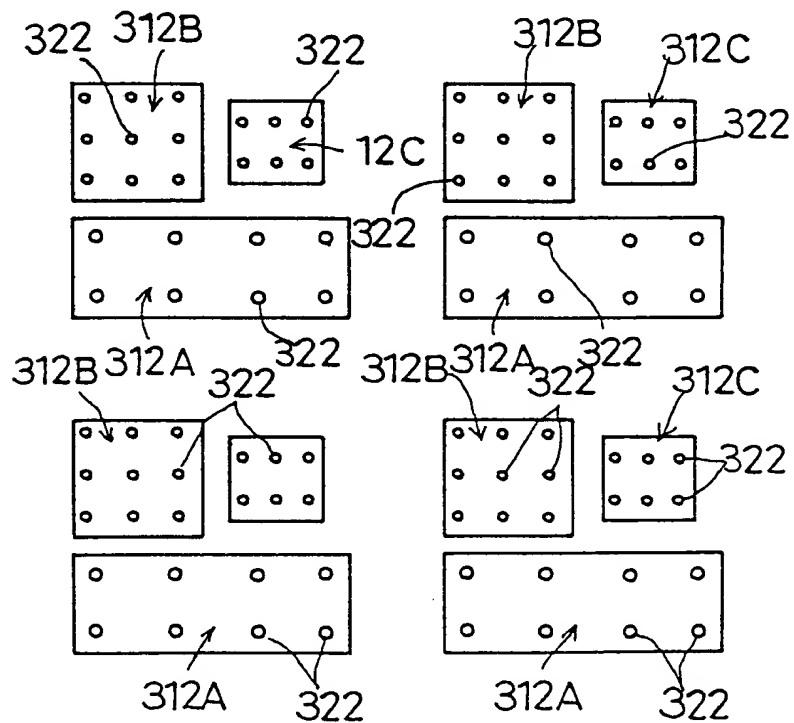


图 125

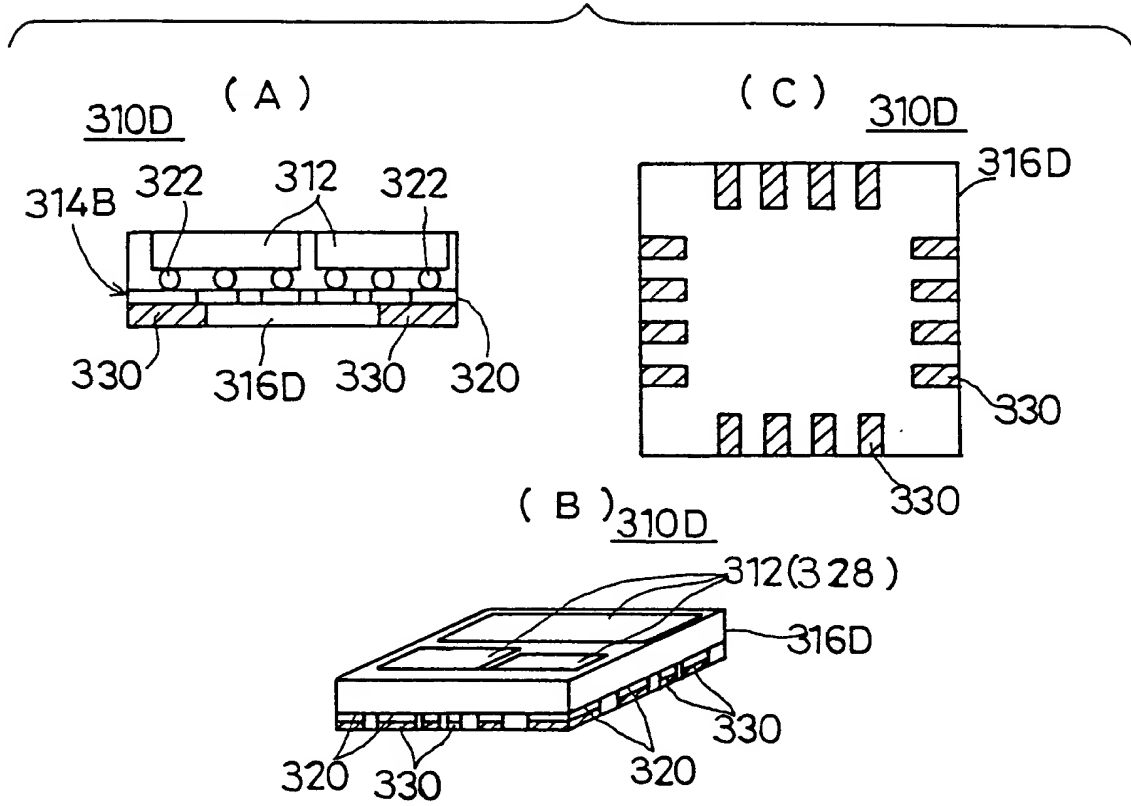


图 127

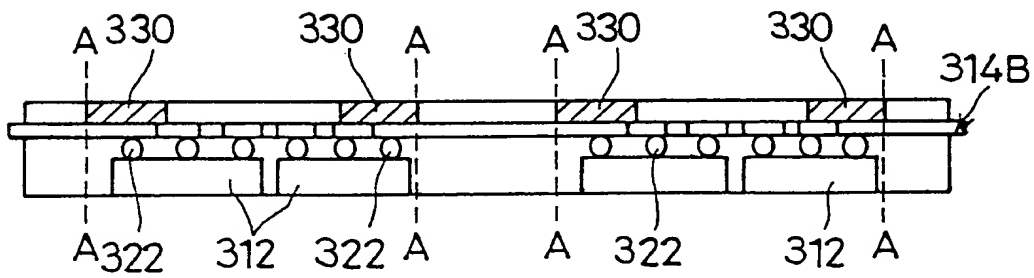
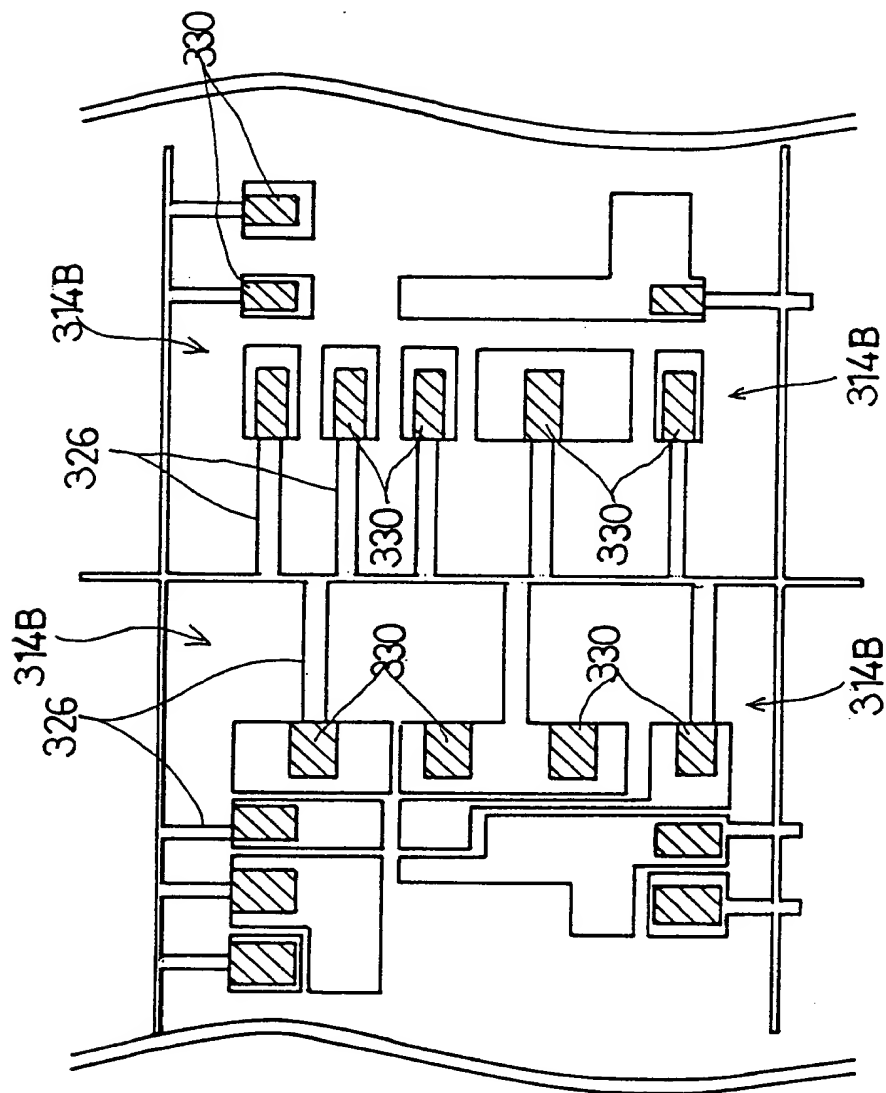
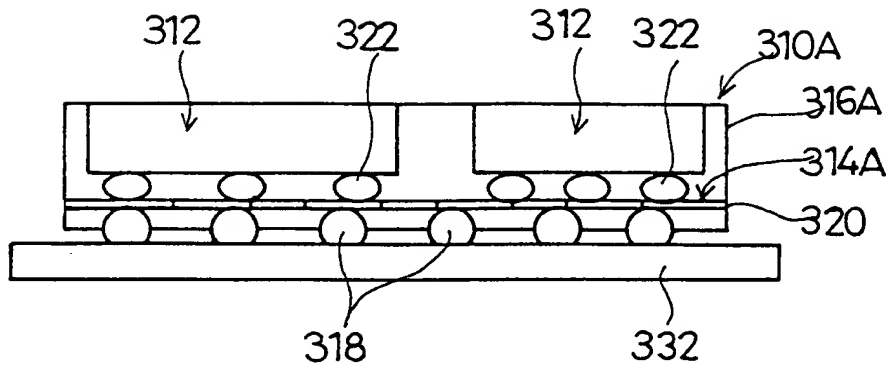


图 126

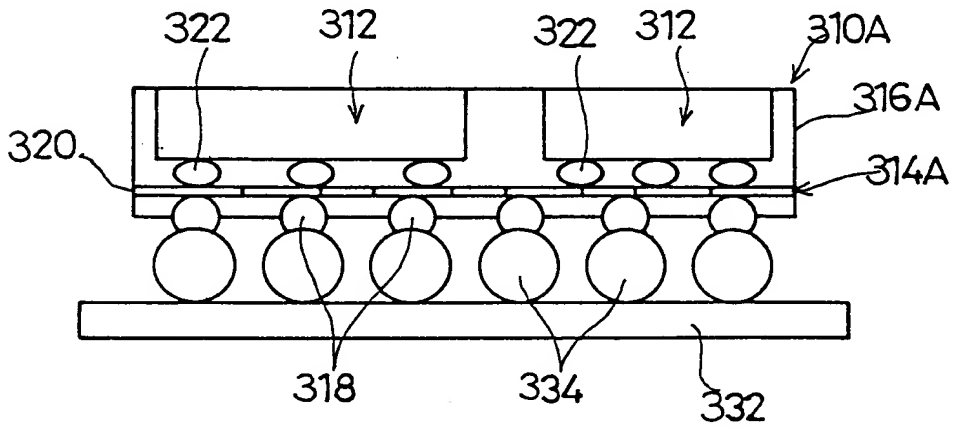
324B



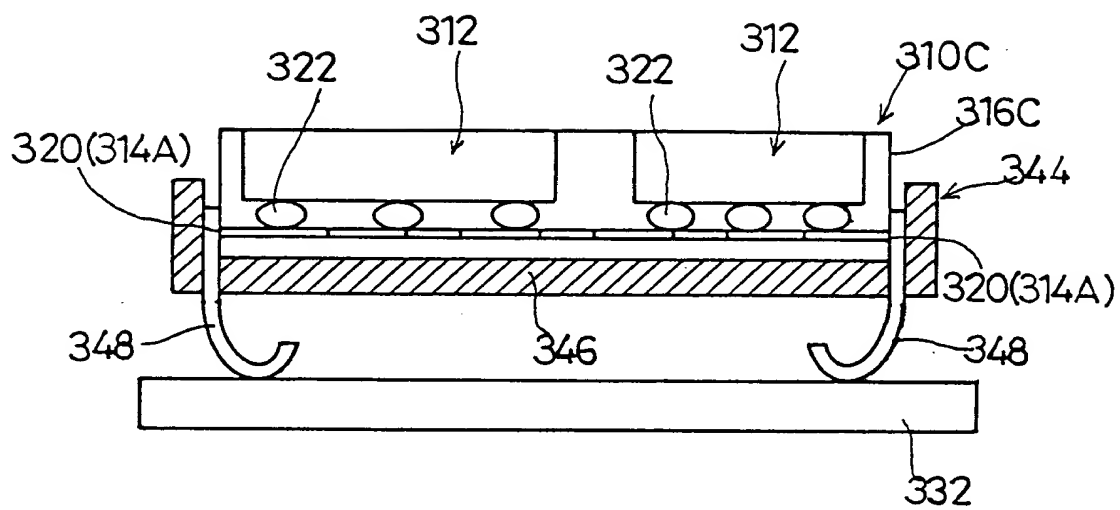
128



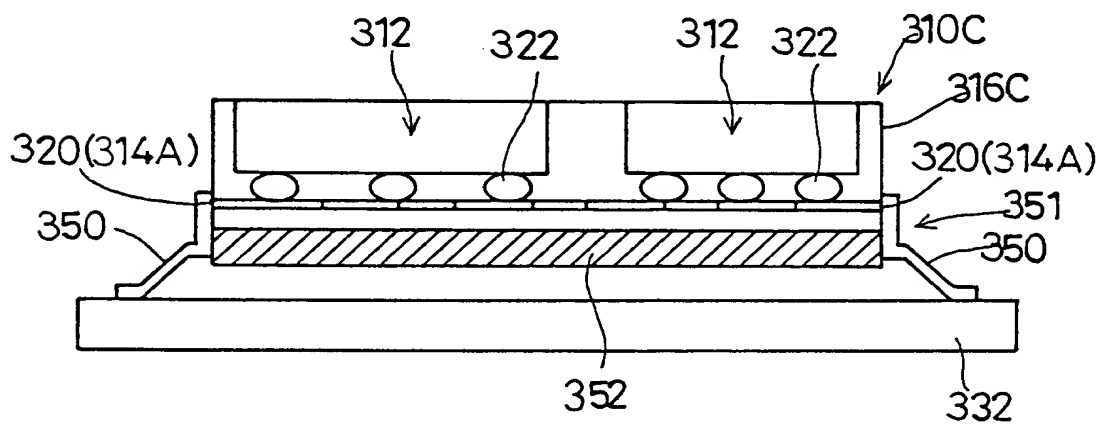
129



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133



136

324A

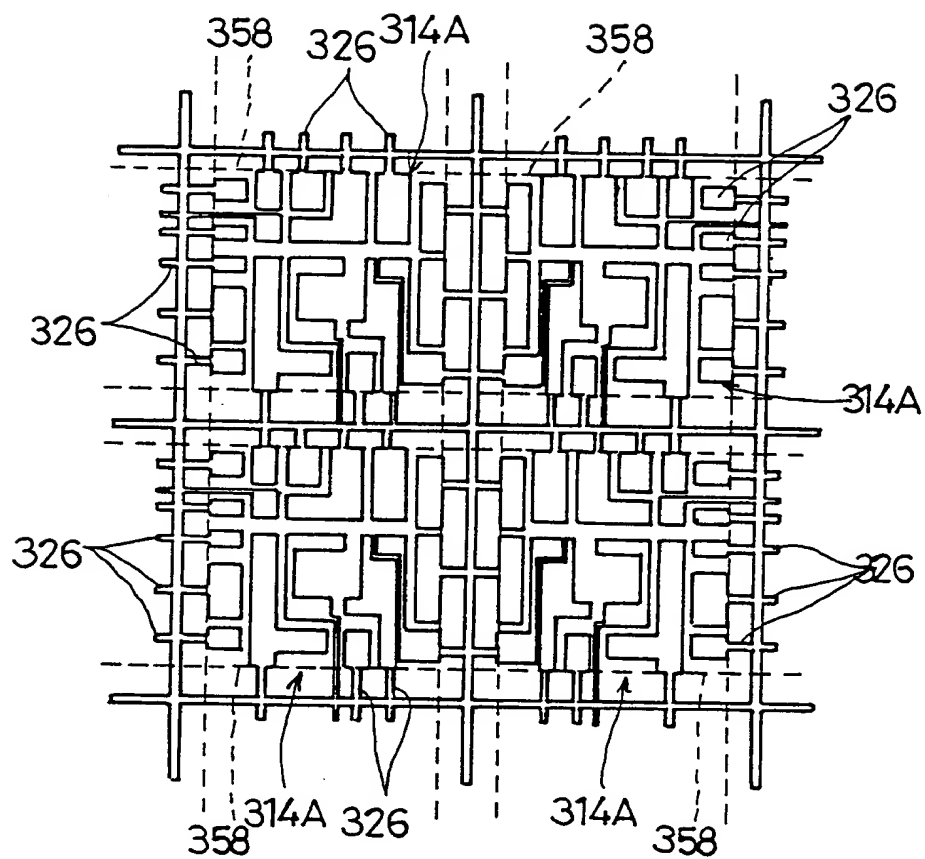


图 137

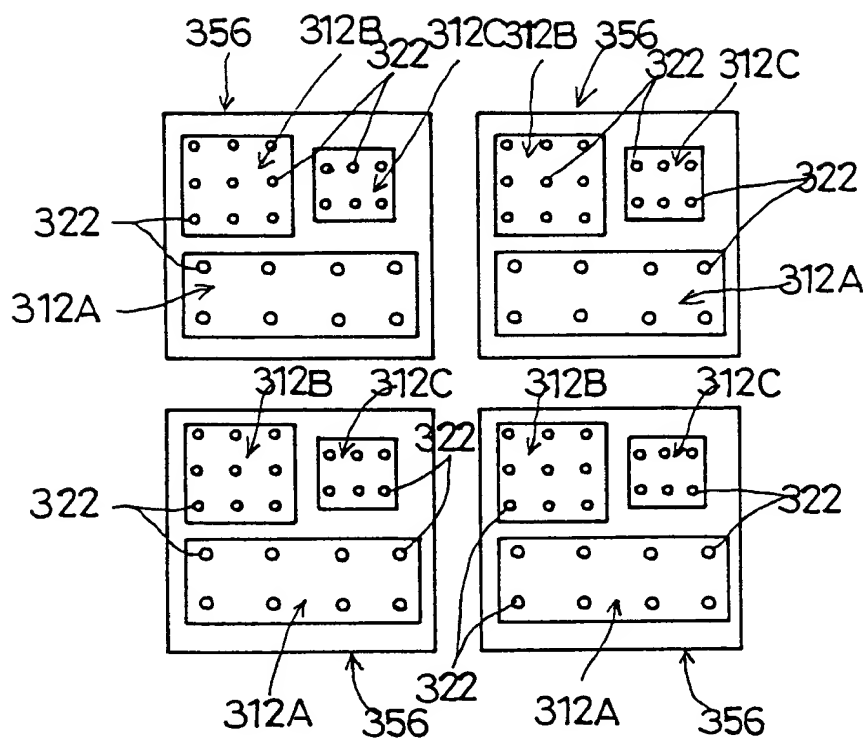


图 138

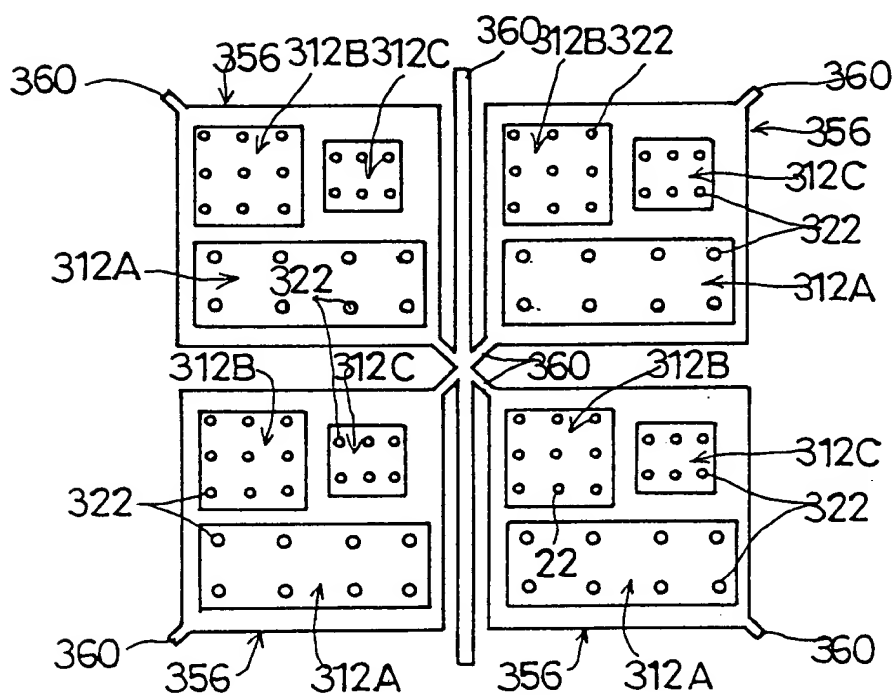


図 139

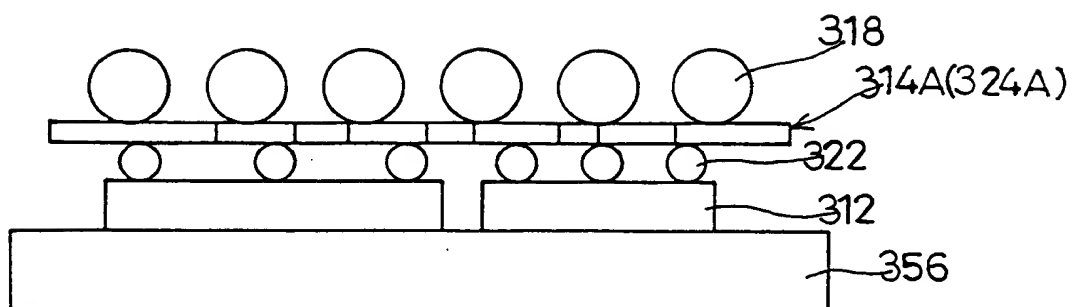


図 140

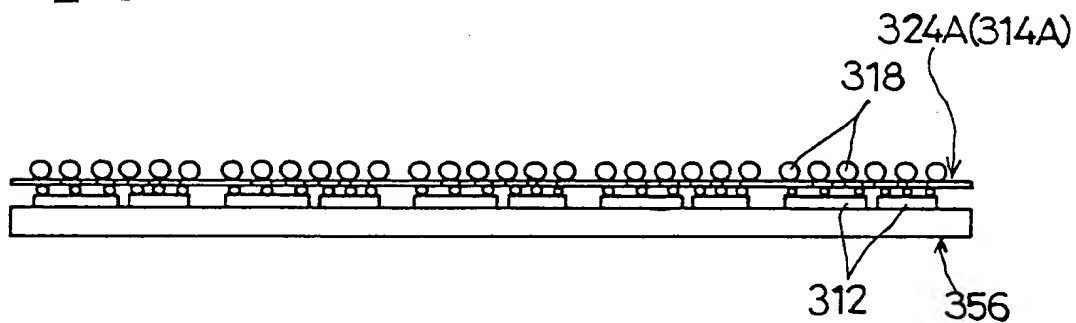


図 141

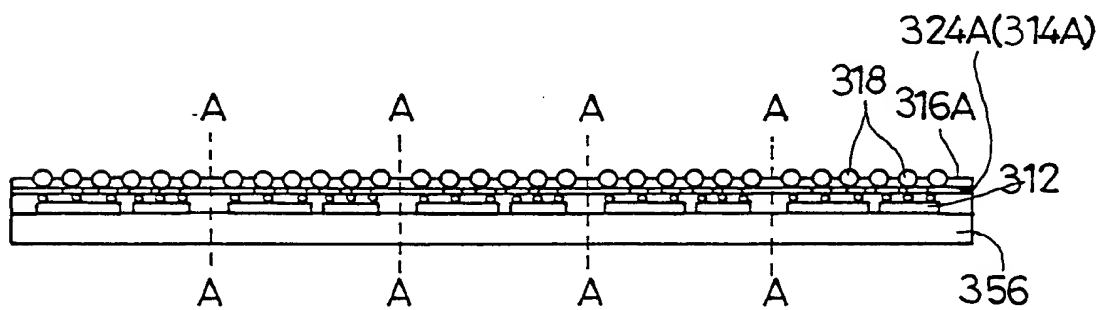


图 143

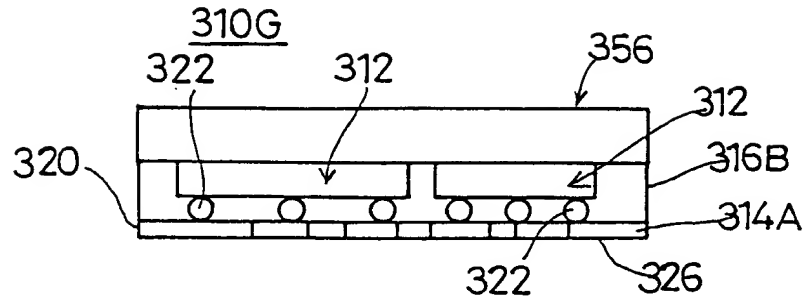


图 144

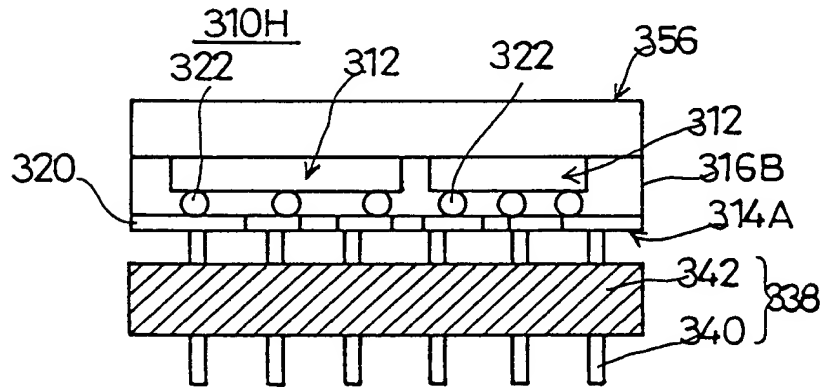
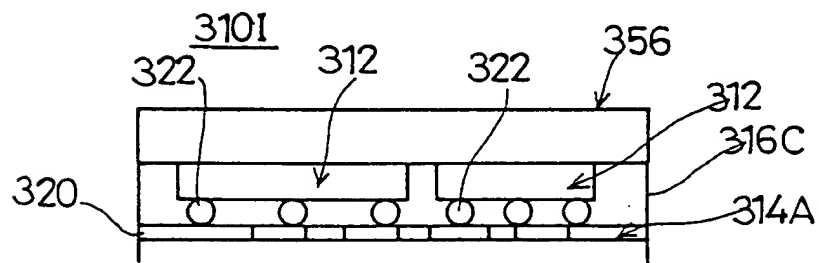
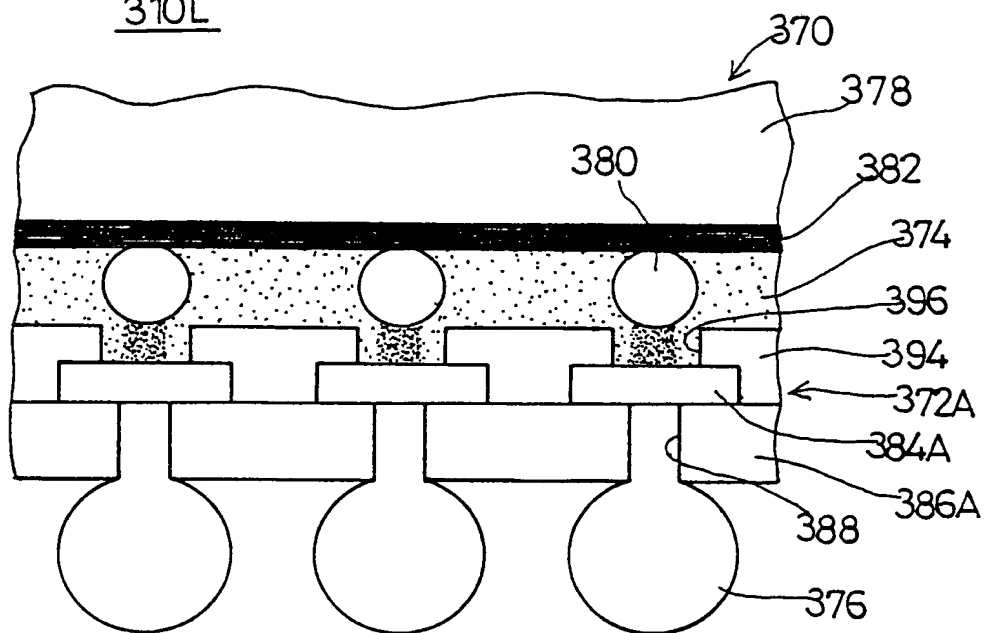


图 145



149

310L



150

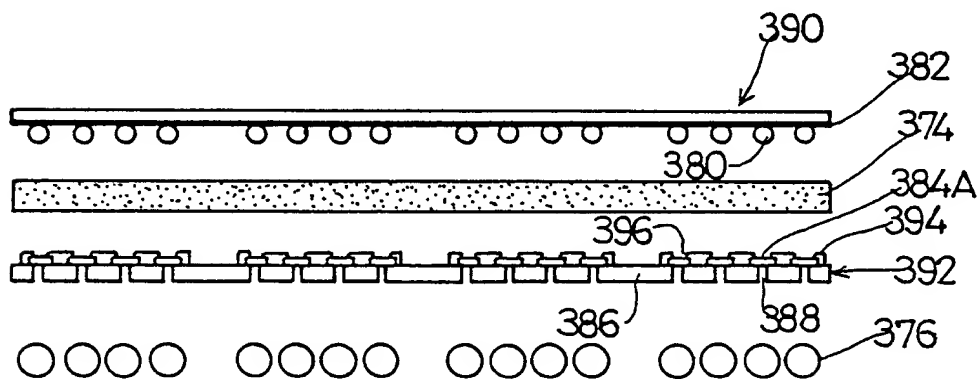


图 156

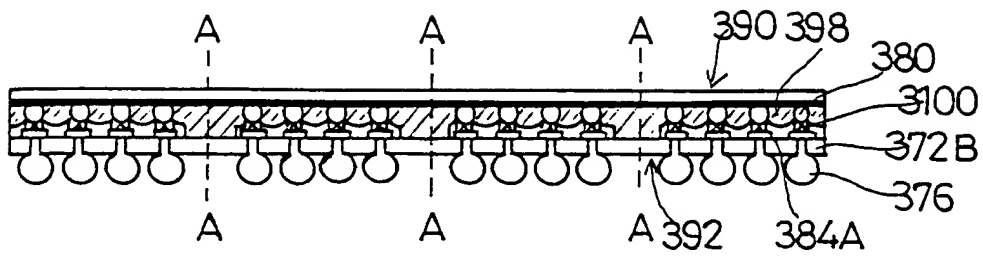


图 157

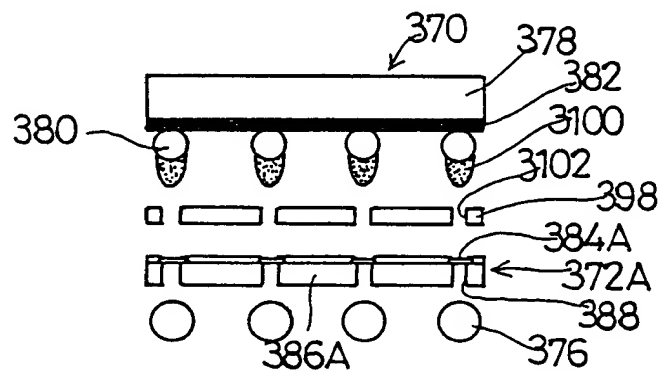
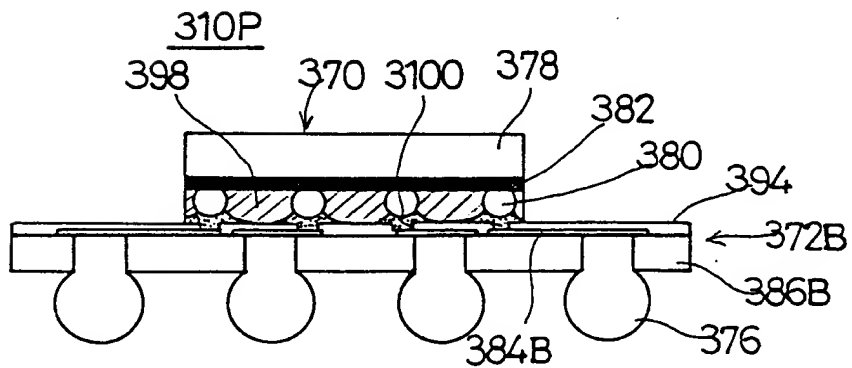
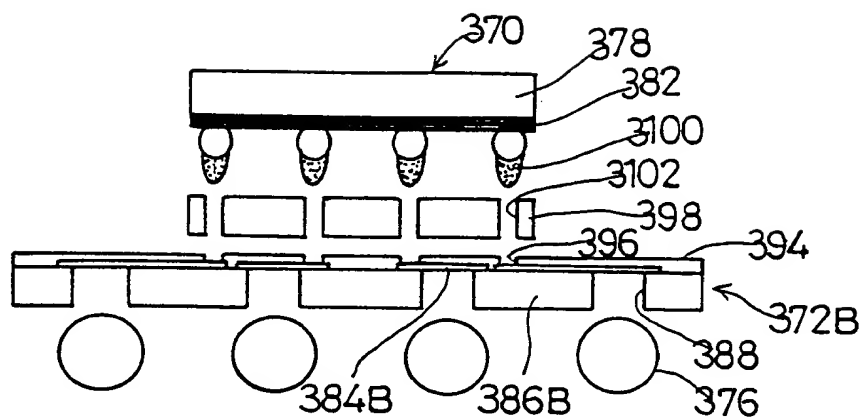


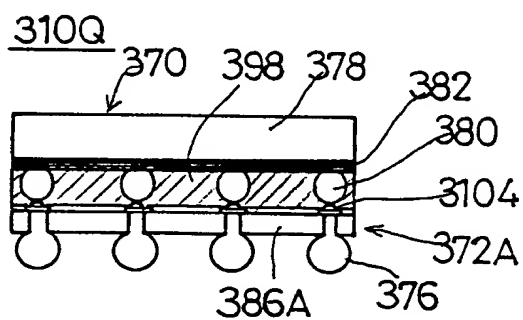
图 158



159



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161

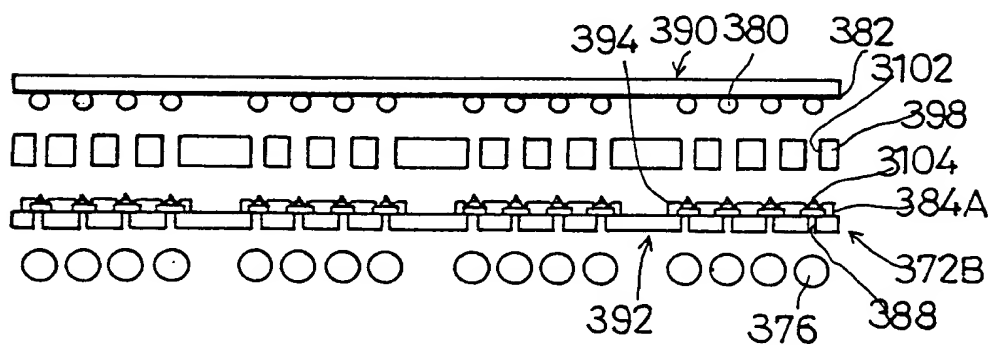


图 162

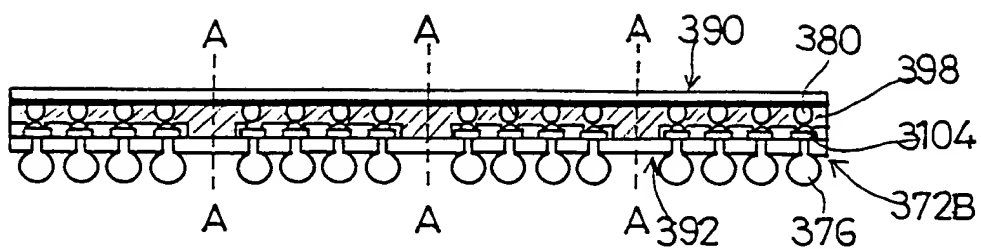


图 163

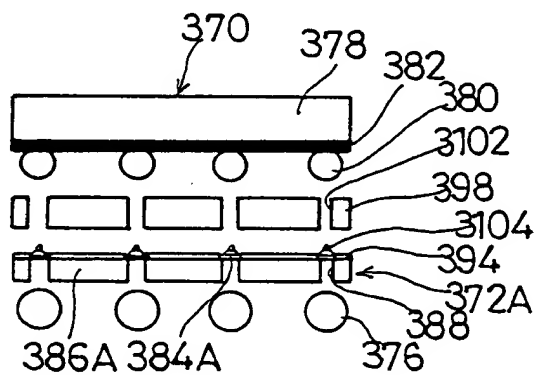


图 164

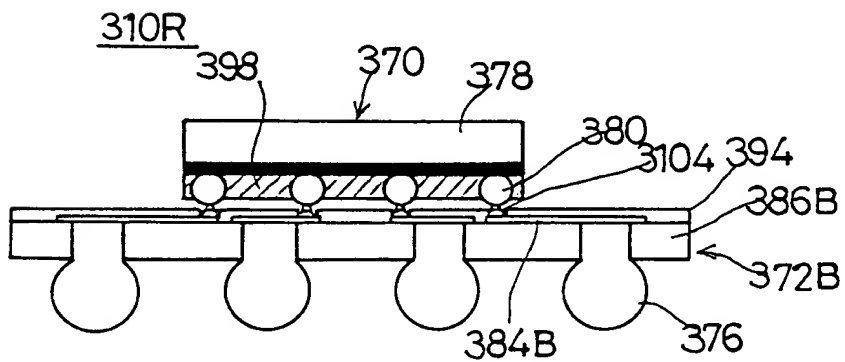


図 165

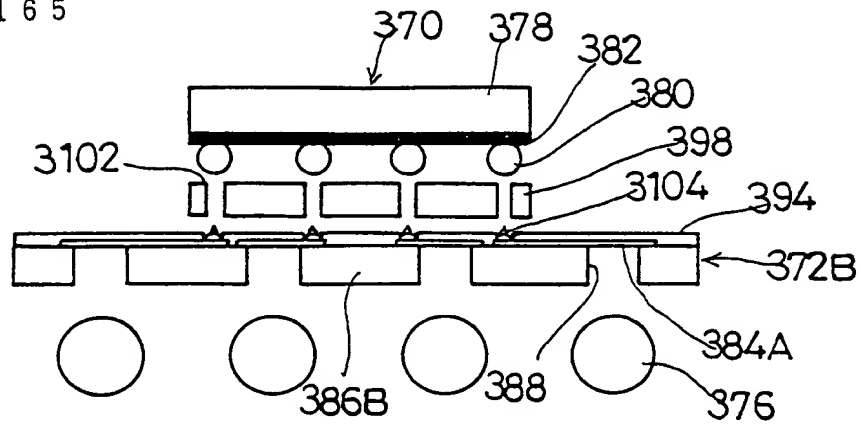


図 166

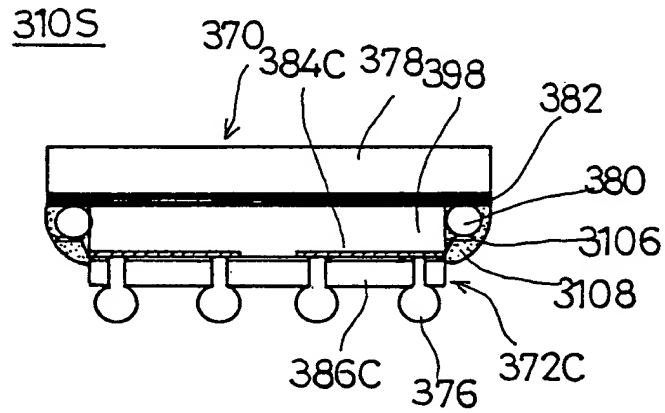


図 167

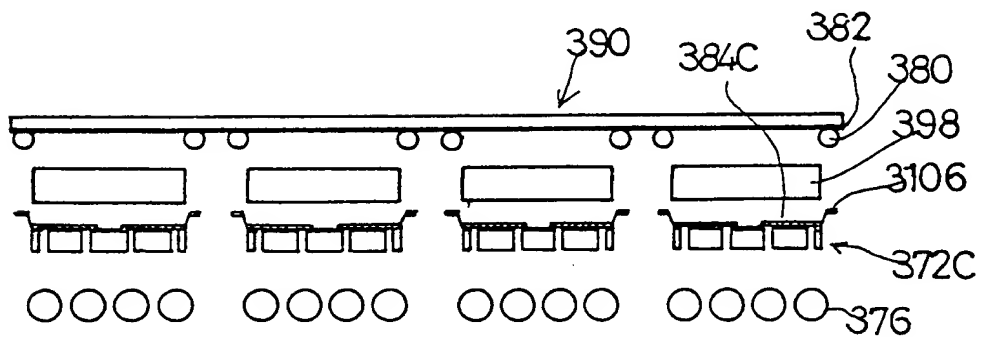


图 168

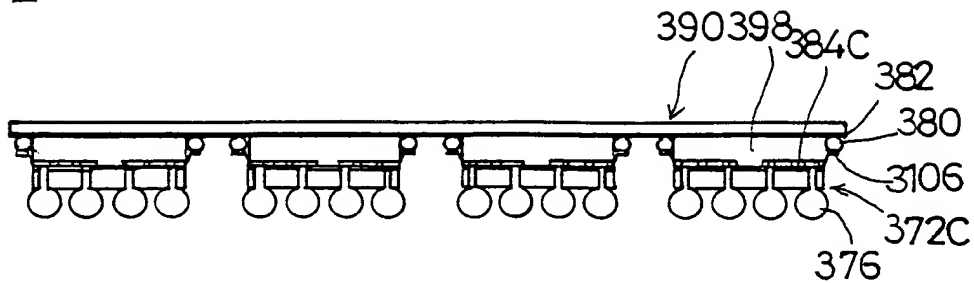


图 169

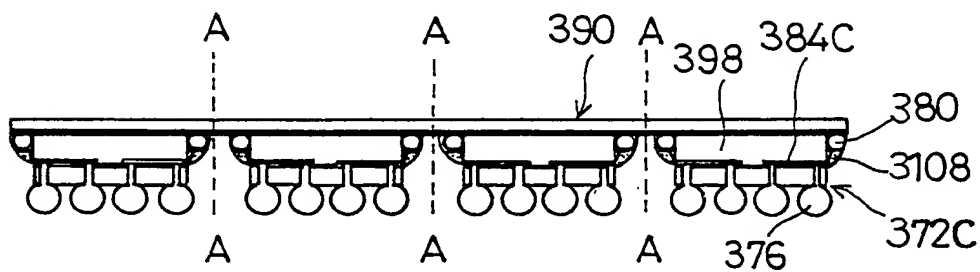
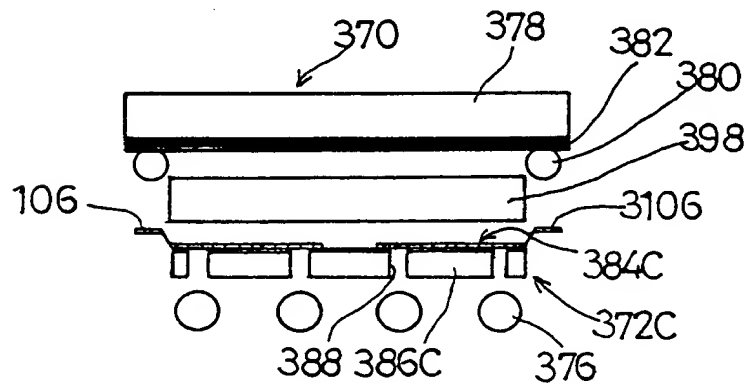
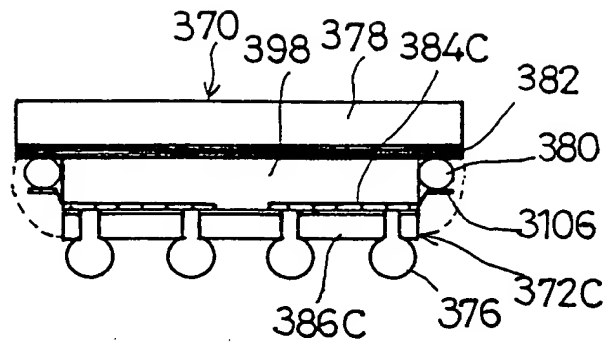


图 170

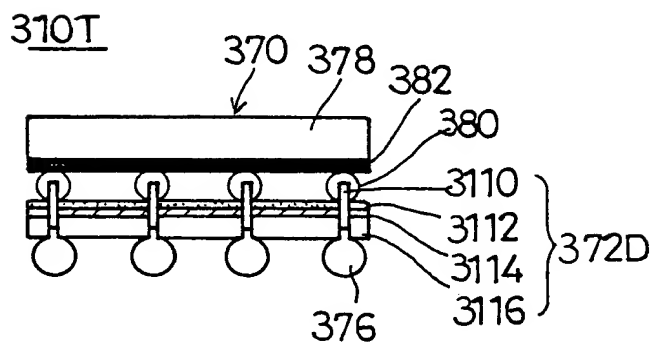


171

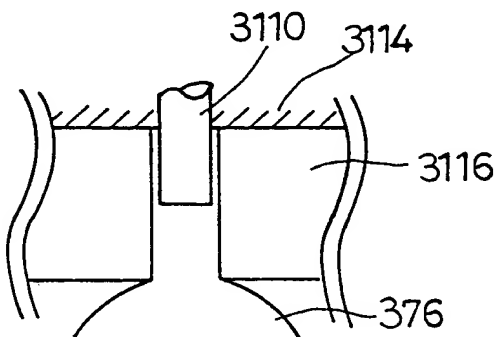


172

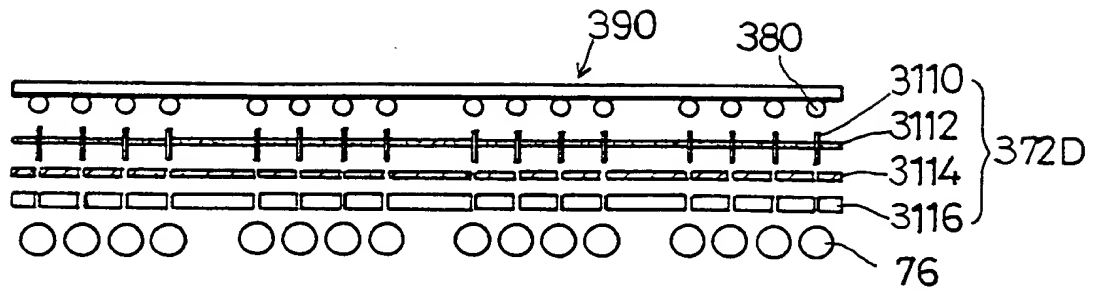
(A)



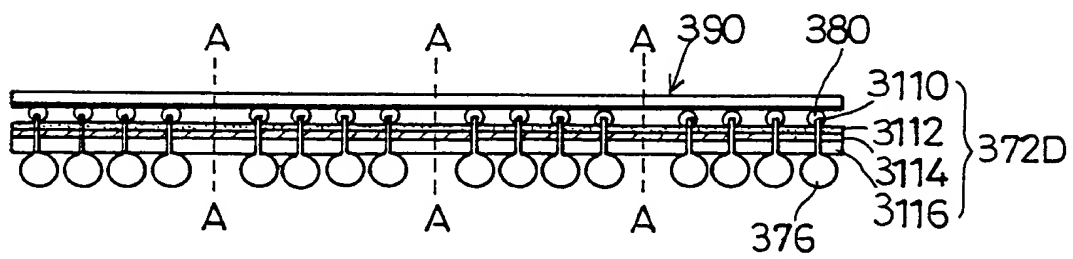
(B)



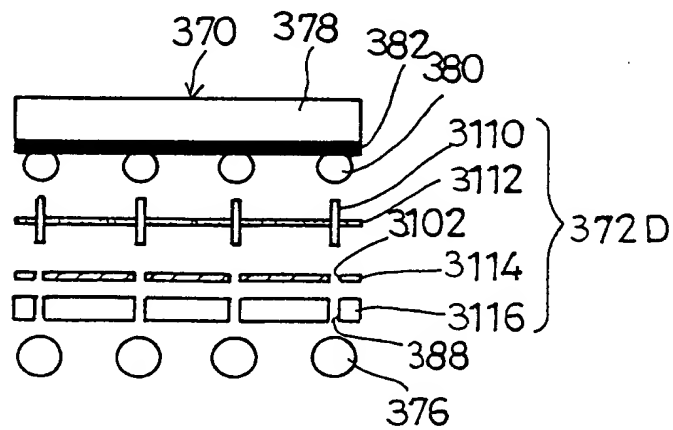
173



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图 176

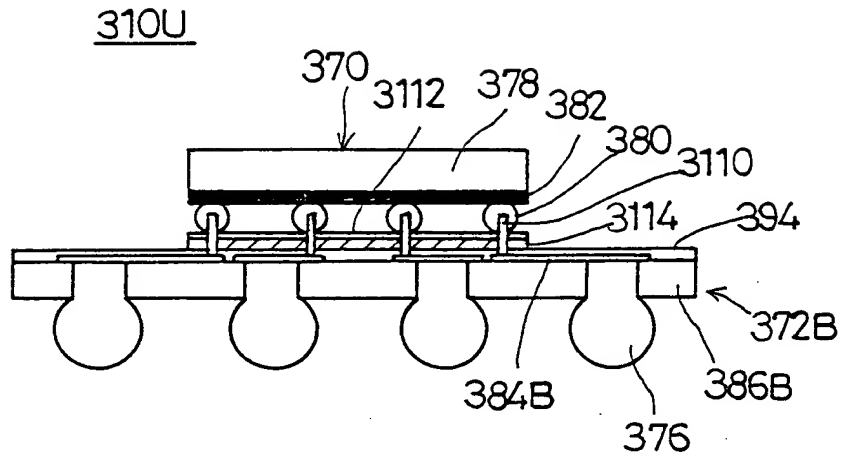


图 177

